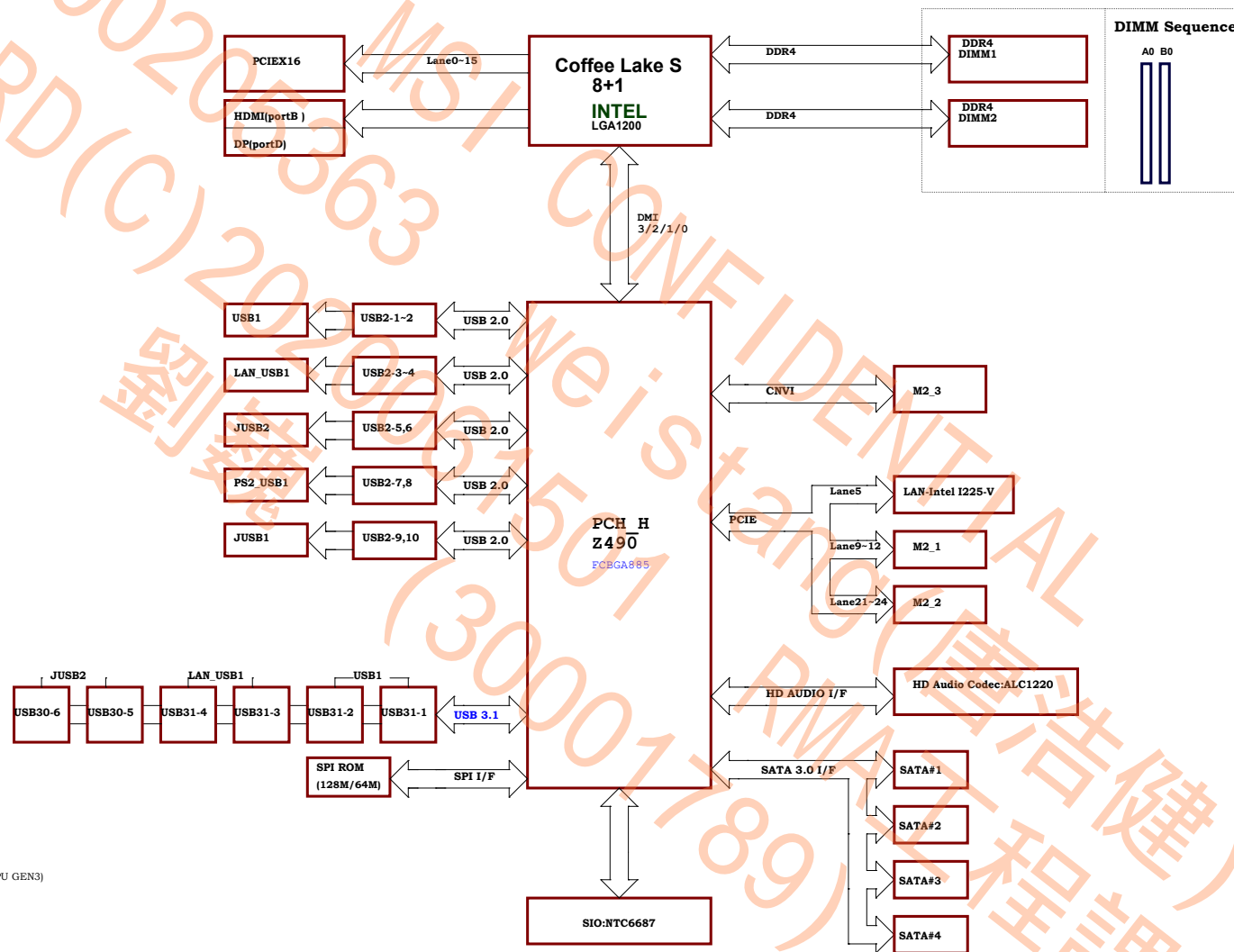
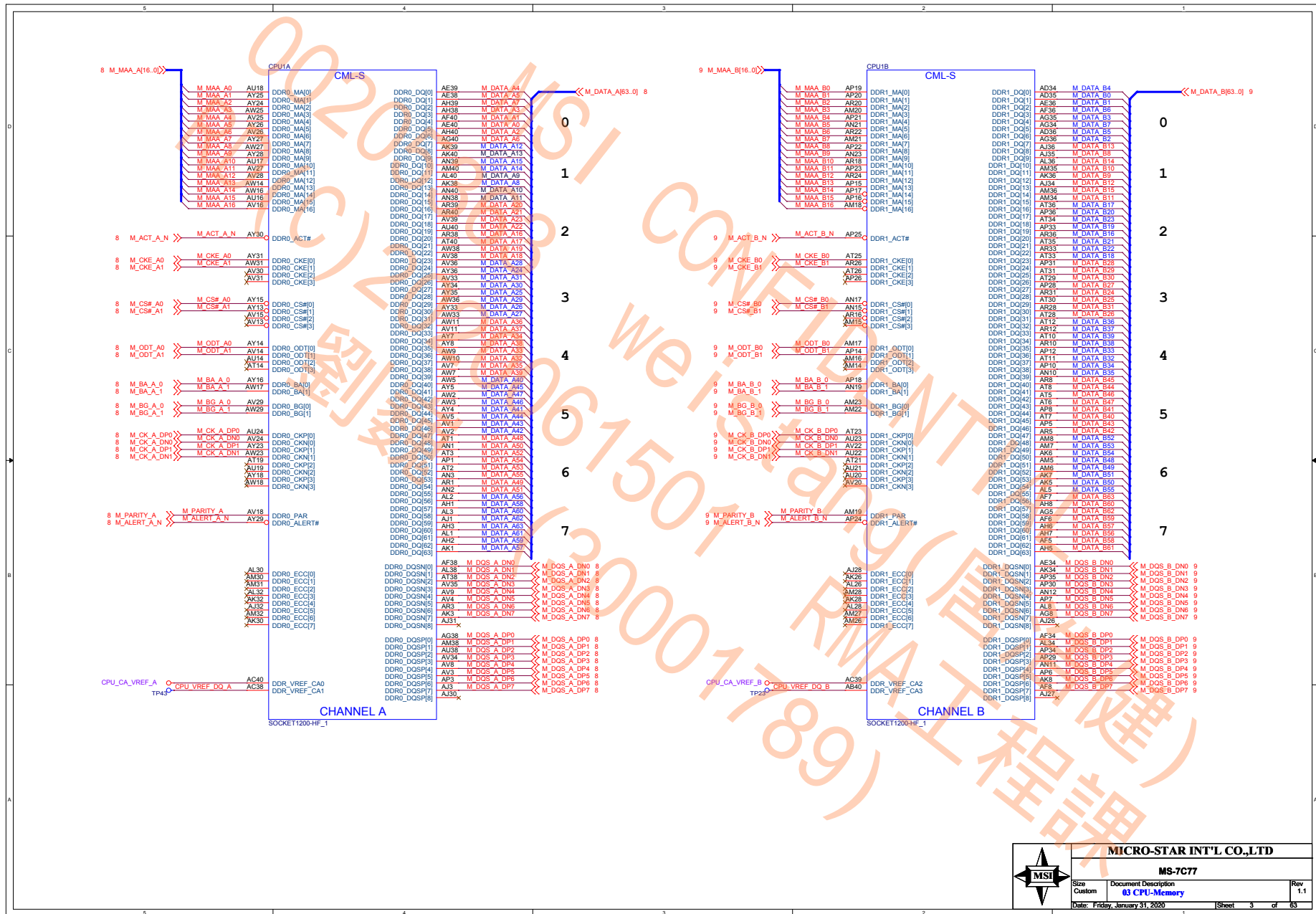


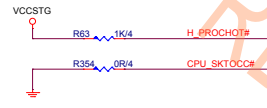
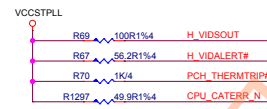
TITLE		Page	MS-7C77		ITX:170*170
Cover Sheet		1			Ver: 1.1
Block Diagram		2	CML Platform		
CPU-Memory, CPU-Control/MISC/CFG/Audio		3,4	CPU: Comet lake S		System Chipset: Z490 PCH_H
CPU-PEG/Display/RSVD		5	LGA1200		
CPU-Power,CPU-GND		6,7	CPU POWER PAK *8 Phase		
DDR4 DIMM1&DDR4 DIMM2 & DDR4 POWER		8,9,10,11	GT POWER PAK *1 Phase		
PCH-LPC/SPI/SMBUS/MISC		12	Onboard Chip: SIO:NCT6687		
PCH-Audio/Display/Clock		13	HD Audio Codec:ALC1220		
PCH-DMI/PCIE/USB/SATA		14	LAN-Intel I225-V		
PCH-GPIO/RSVD/CNVI		15	Flash ROM: SPI 128 MB X1		
PCH-Power/Gnd/Strip		16,17,18	Main Memory: DDR4 * 2 (Dual Channel)		
PCIE SLOT-CPU(X16)		19	ACPI:		Power:
SIO-NCT6687D		20,21	5VDAUL:uP7501		VCORE/GT/SA - ISL69269
SPI ROM BIOS/ FAN VBAT/CLR COMS		22,23,24,25	5VDIMM:uP7501		VCCIO -RT8125E
MCU JRAINBOW/DEBUG LED		26	3VSB:TPS566235		VCCST/VCCSTG-MP2333
AUDIO - ALC1220P		27,28	3VDSW:GS7133		VCCPLL/VCCPLL_OC-GS7133
TR DP / PD		29,30,31,32	SIO_3VA:GS7116		DDR - RT8231
LAN - INTEL I255V		33			PCH(1.05V) - RT8125E
Rear LAN USB3.1&USB2.0		34			1P8_VSB - GS7133
Rear-USB31 TYPEA+TYPE C/Front USB 3.2/2.0		35,36	Expansion Slots:		
Front USB31 TYPE C / USB POWER		37,38	PCI Express (X16) Slot * 1		
M2 Connector/SATA/CNVI		39,40,41,42			
HDMI Connector/DP		43,44			
ACPI CONTROLLER		45			
PWR-ISL69269/CORE-PH1-8/GT PH1/SA PH1		46,47,48			
PWR-VCCIO-RT8125E/PWR-VCCST/PLL		49,50			
DDR-RT8231/VPP25-MP2333		51,52			
PCH RT8125E/1P8_VSB/PWR-VRM/PCH Sequence		53,54			
OV-NCT3933/ATX F_Panel		55,56			

# MS-7C77 Block Diagram

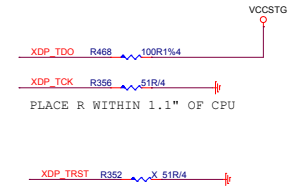
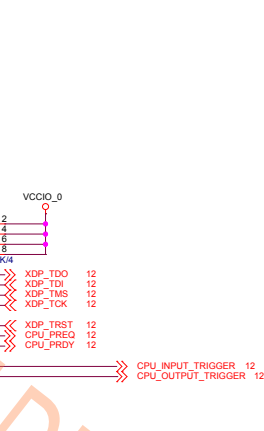
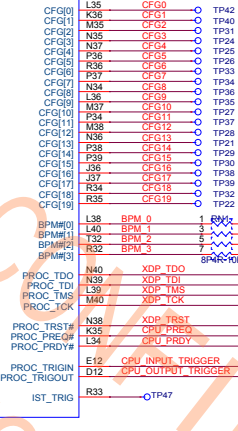
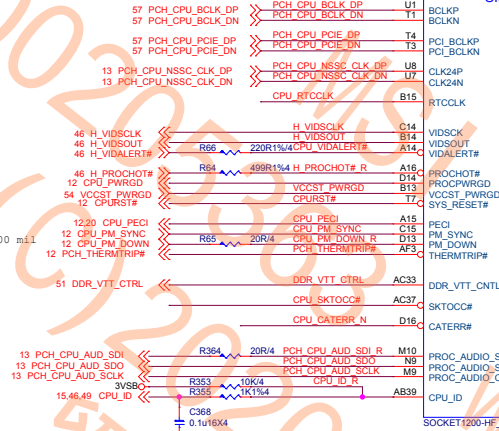


**Slot Sequence:**  
**PCIEX16** (By CPU GEN3)





SIO CPU\_PM\_DOWN\_R < 200 m11

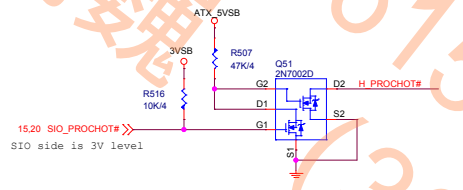


PLACE R WITHIN 1.1" OF CPU

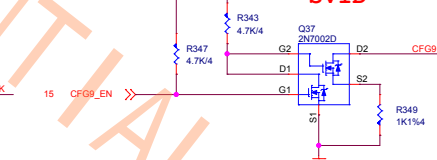
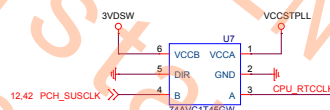
# CFG Strap

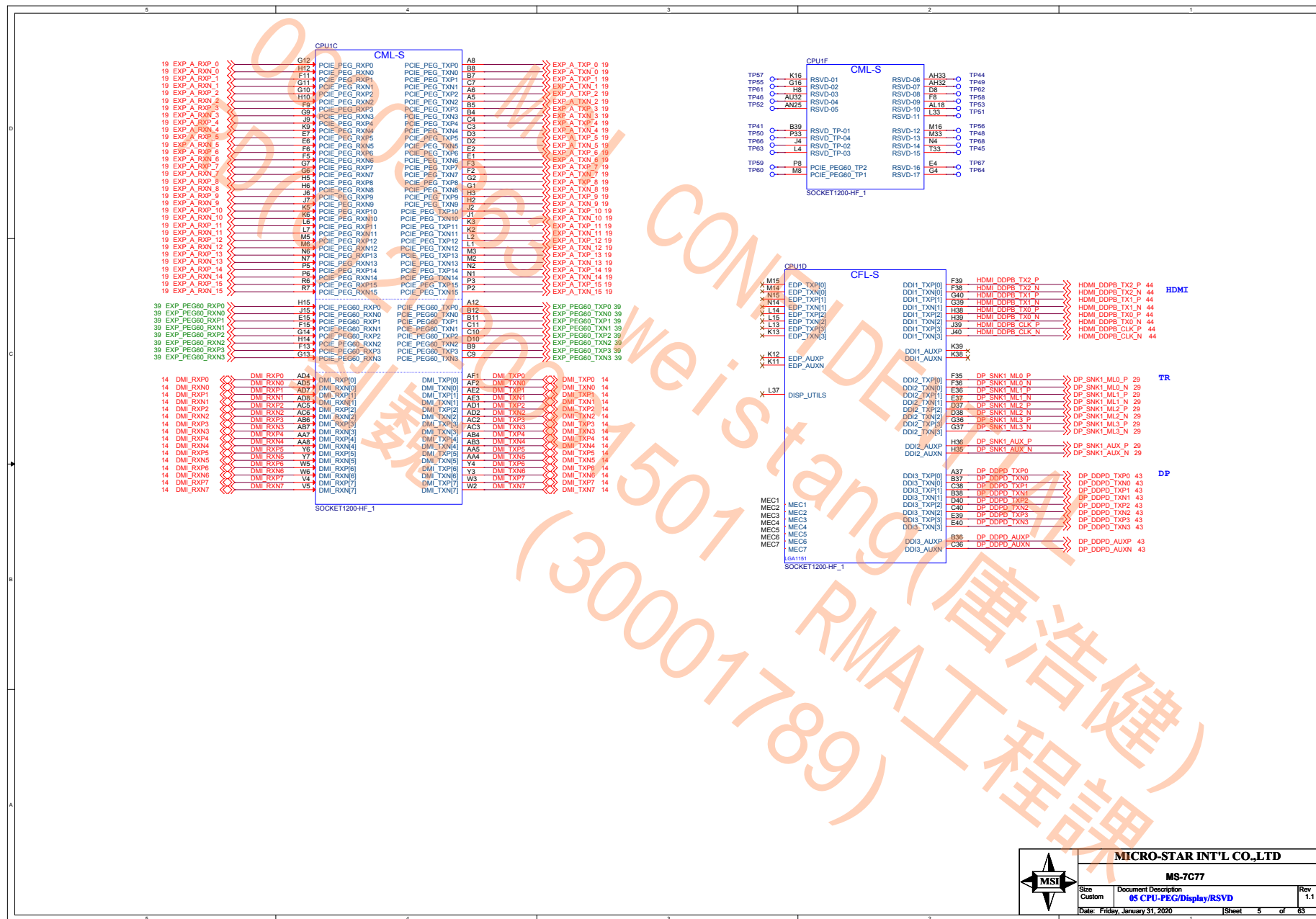
CFG Table		
HIGH	LOW	Intel
0 (Default)Normal Operation	stall	PCU PLL lock
1	REVERSE	REV0
2	REVERSE	PCU LANE REVERSAL
3	REVERSE	REV0
4	ENABLE	REV0
5	ENABLE	PCUCFSEL[0]
6	ENABLE	PCUCFSEL[1]
7	ENABLE	REV0
8	BIOS REQ	REV0
9	PRESENT	NO PRESENT
10	PRESENT	NO PRESENT
11	PRESENT	NO PRESENT
12	PRESENT	NO PRESENT
13	PRESENT	NO PRESENT
14	PRESENT	NO PRESENT
15	PRESENT	NO PRESENT
16	PRESENT	NO PRESENT
17	PRESENT	NO PRESENT
18	PRESENT	NO PRESENT
19	PRESENT	NO PRESENT

## Temperature protection MP BOM remove



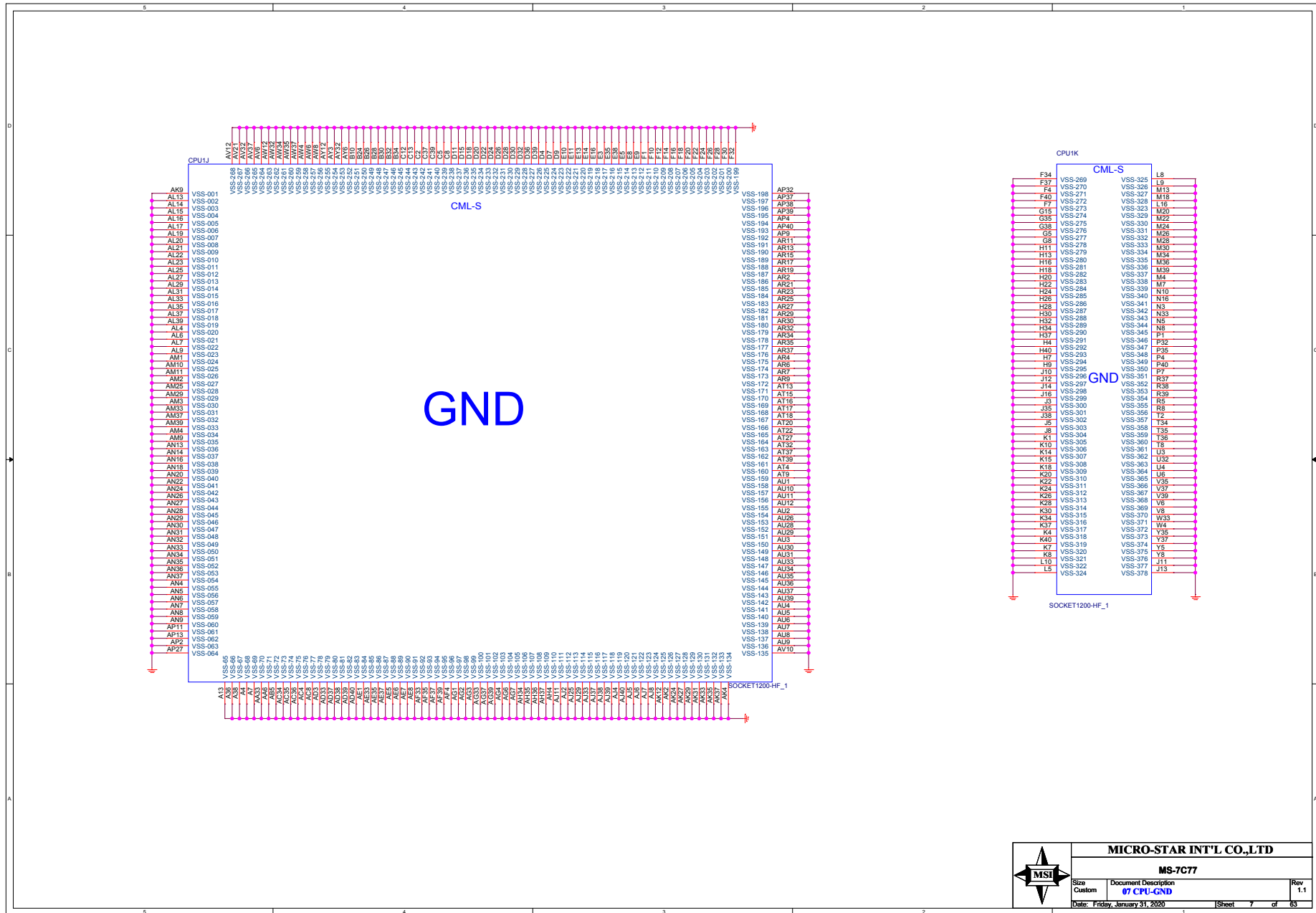
15.20 SIO\_PROCHOT# >>  
SIO side is 3V level







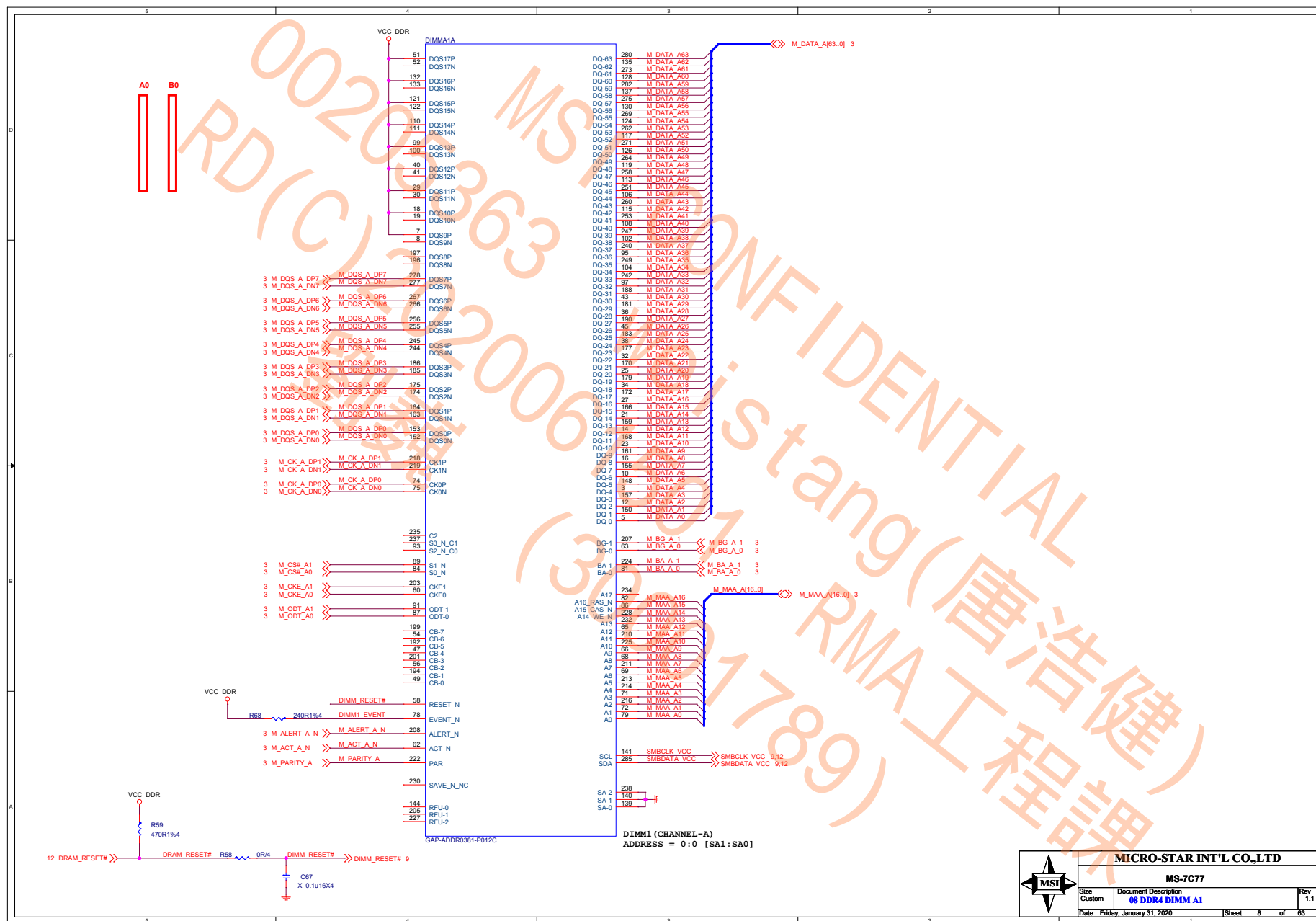




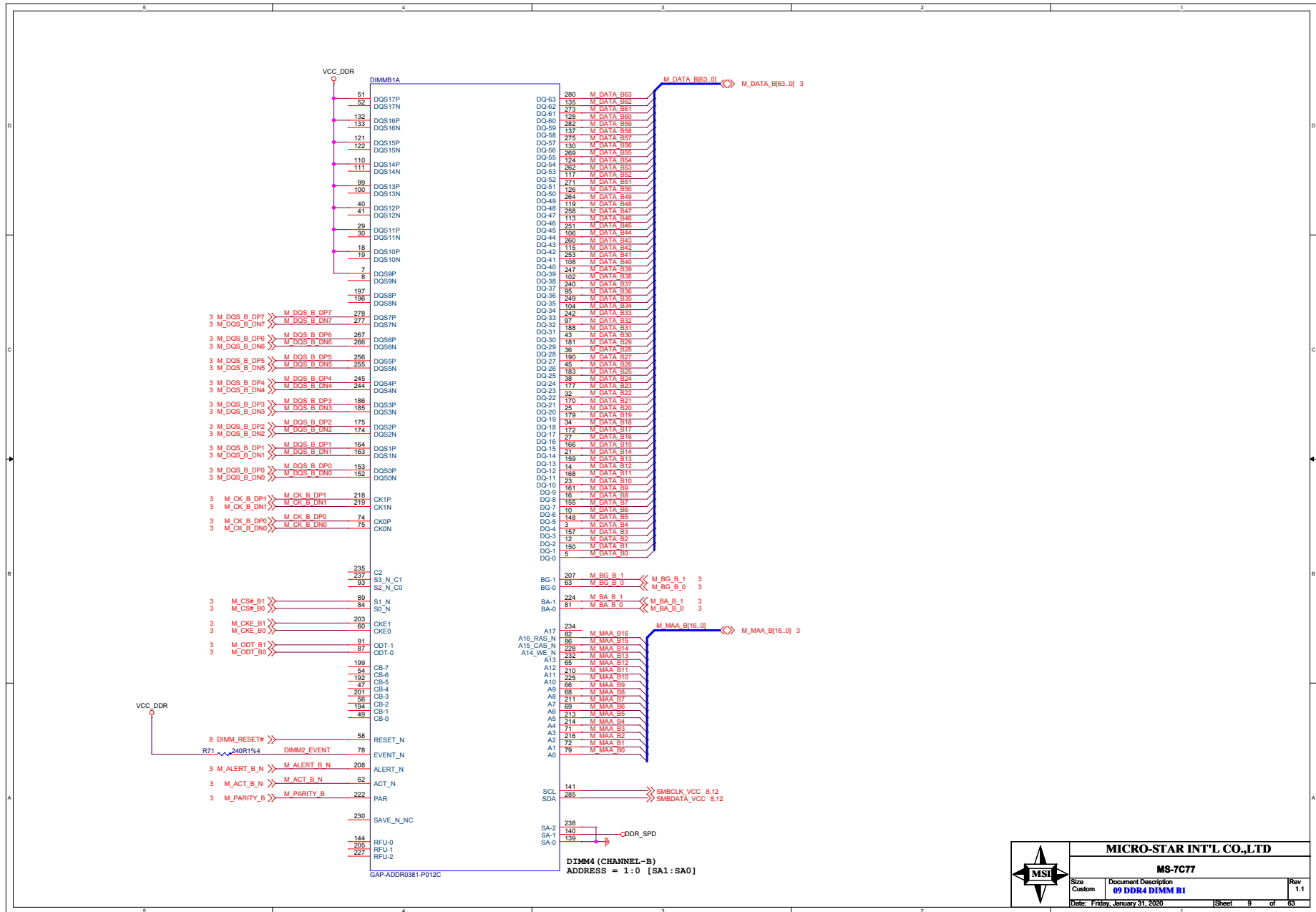
MICRO-STAR INT'L CO.,LTD

MS-7C77

Size	Document Description	Rev
Custom	07 CPU-GND	1.1
Date: Friday, January 31, 2020		Sheet 7 of 63



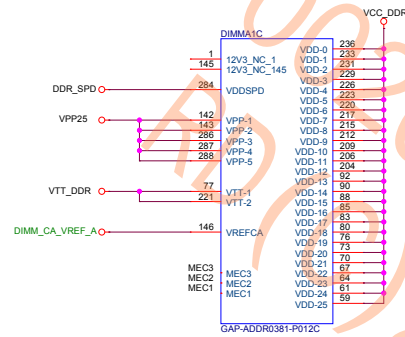




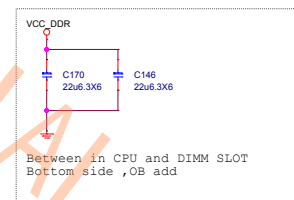
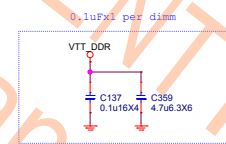
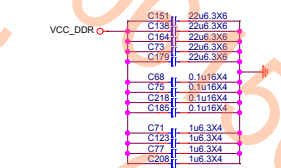
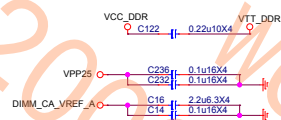
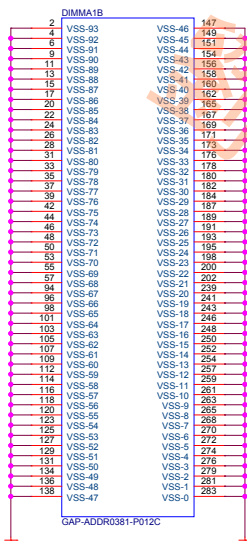
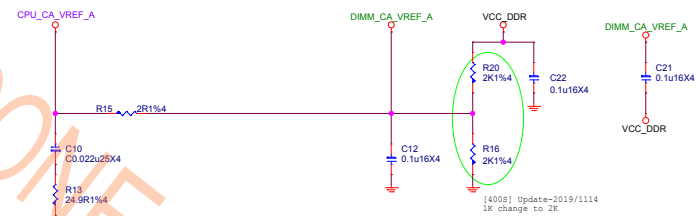
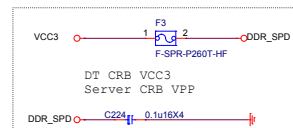
MICRO-STAR INT'L CO.,LTD

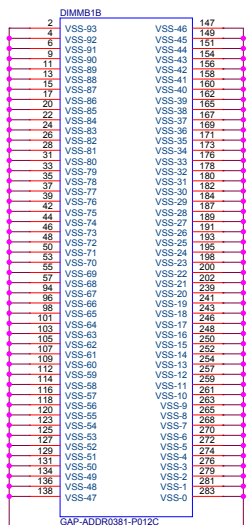
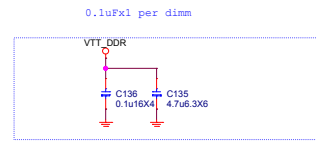
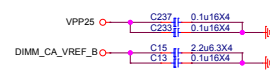
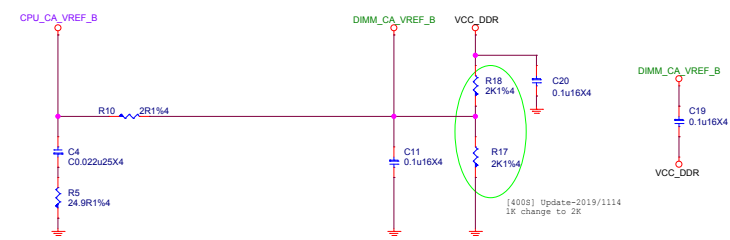
MS-7C77

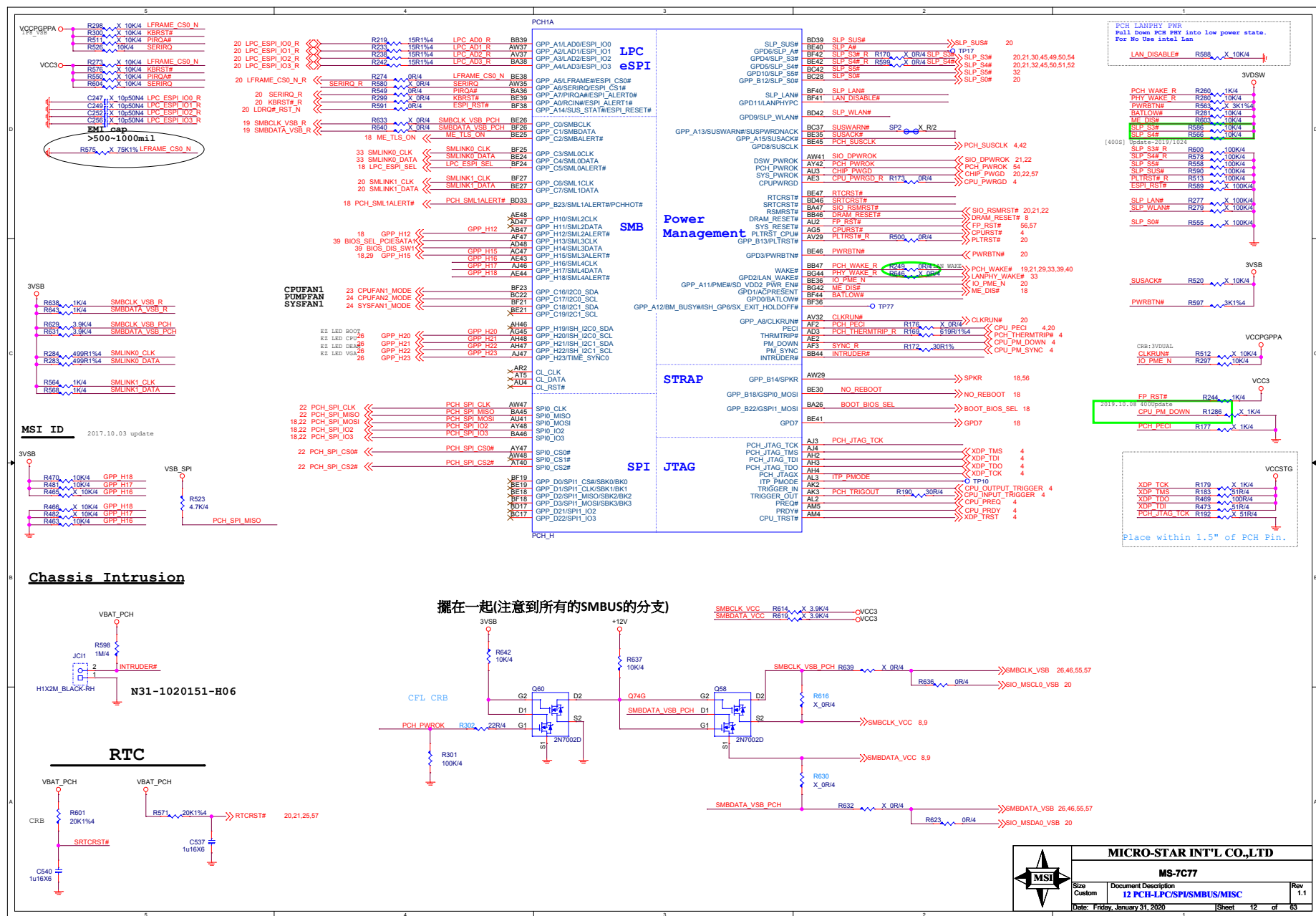
Size Custom	Document Description 09 DDR4 DIMM B1	Rev 1.1
Date: Friday, January 31, 2020		Sheet 9 of 63



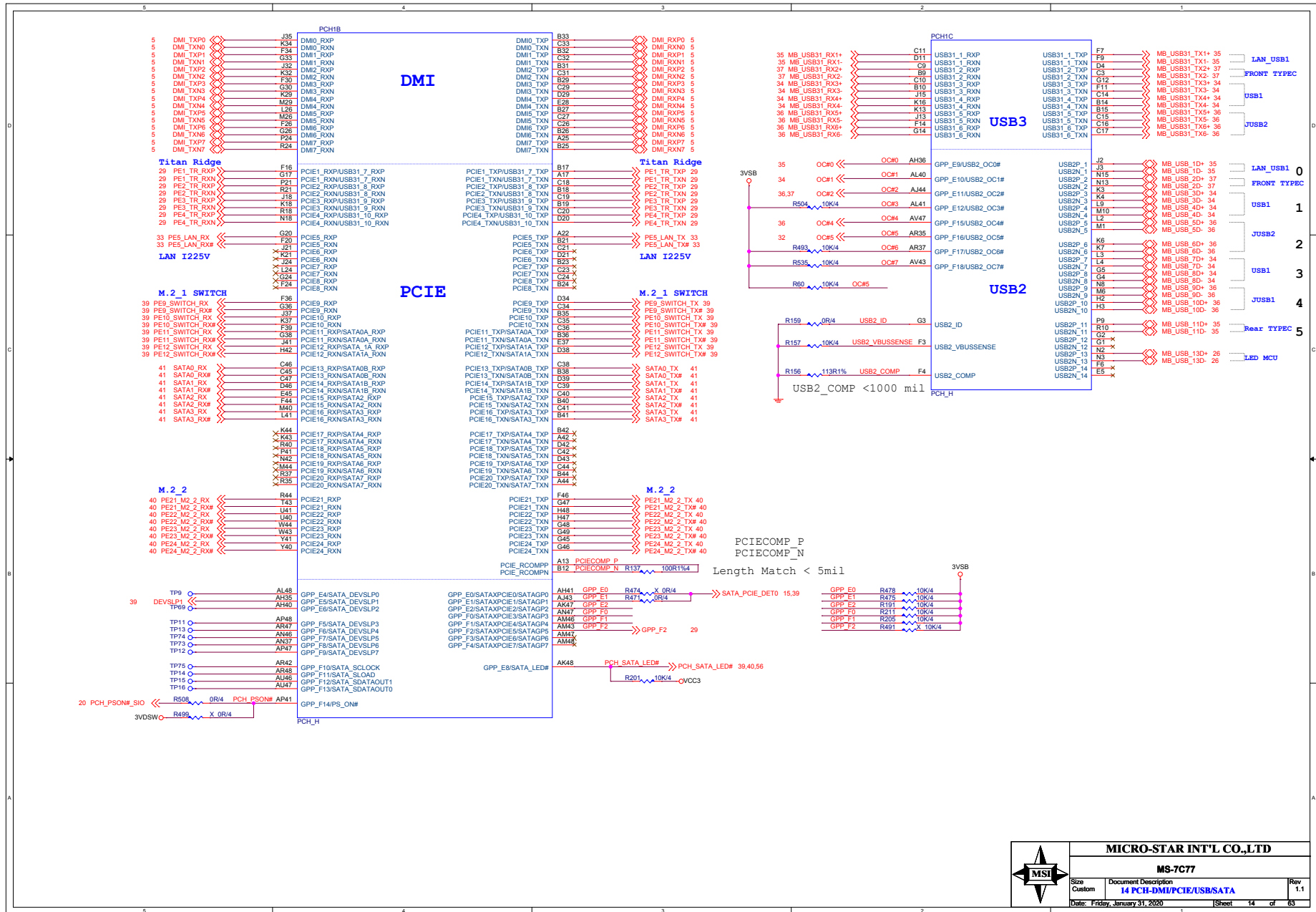
DIMM SLOT PN BY SPEC









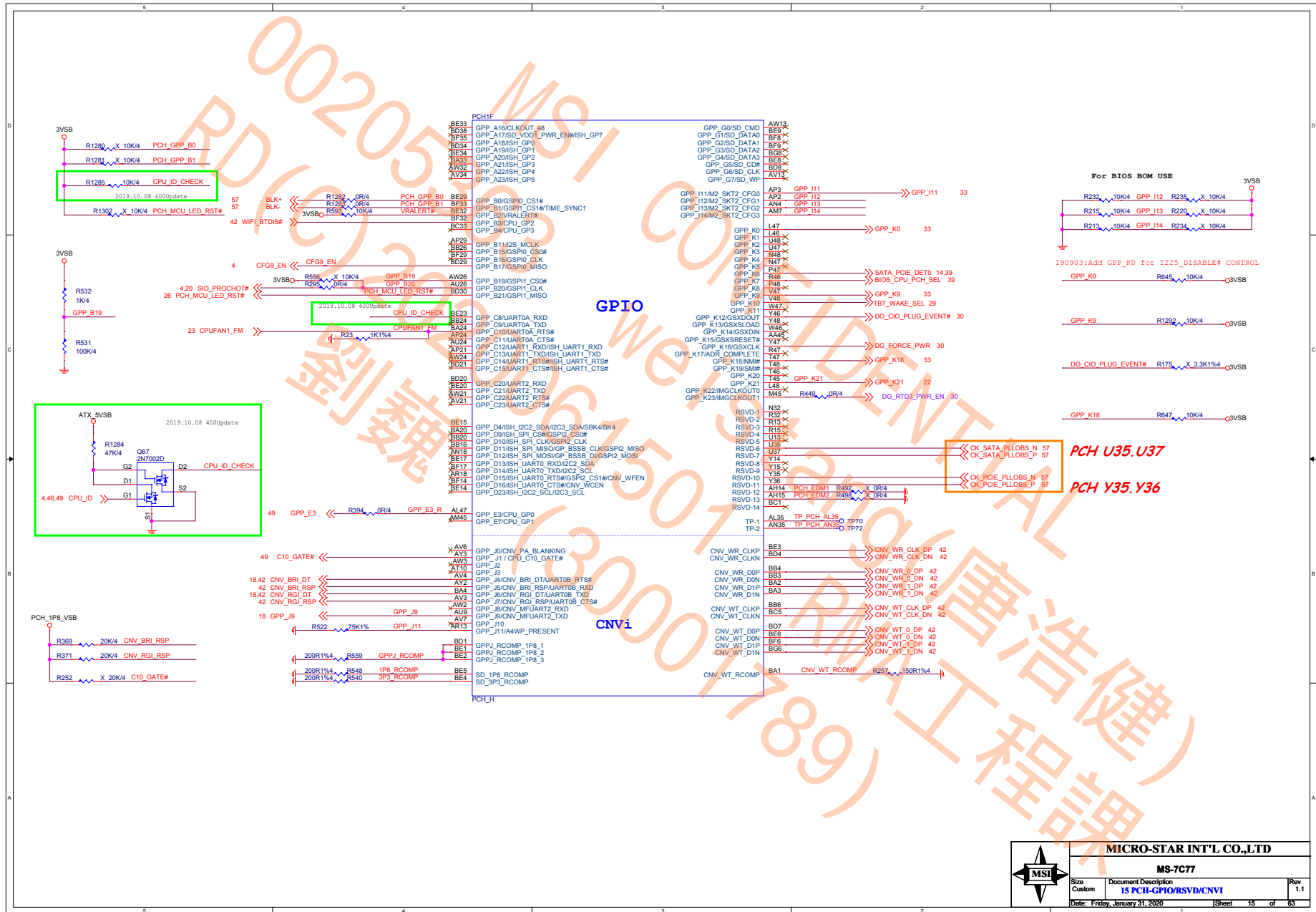


MICRO-STAR INT'L CO.,LTD

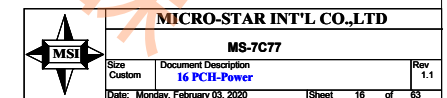
MS-7C77

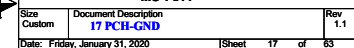
Size	Document Description	Rev
Custom	14 PCH-DMI/PCIE/USB/SATA	1.1
Date: Friday, January 31, 2020		Sheet 14 of 63

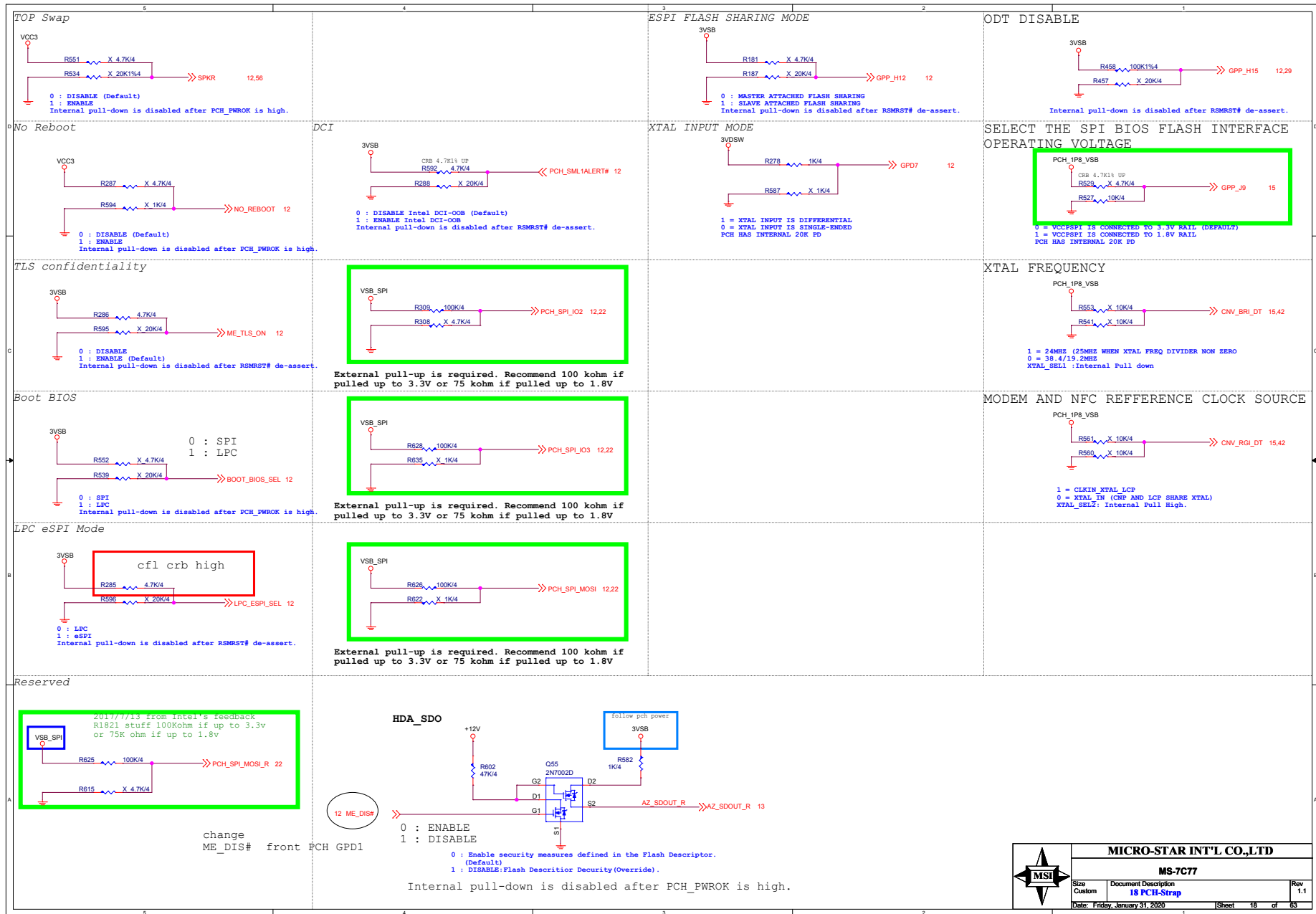




```
Base 7.169A
Base other 1.089A
DMI Gen3 0.5A
PCIE Gen3 1.602A
USB3.1 Gen1 1.062A
SATA3.0 0.668A
```





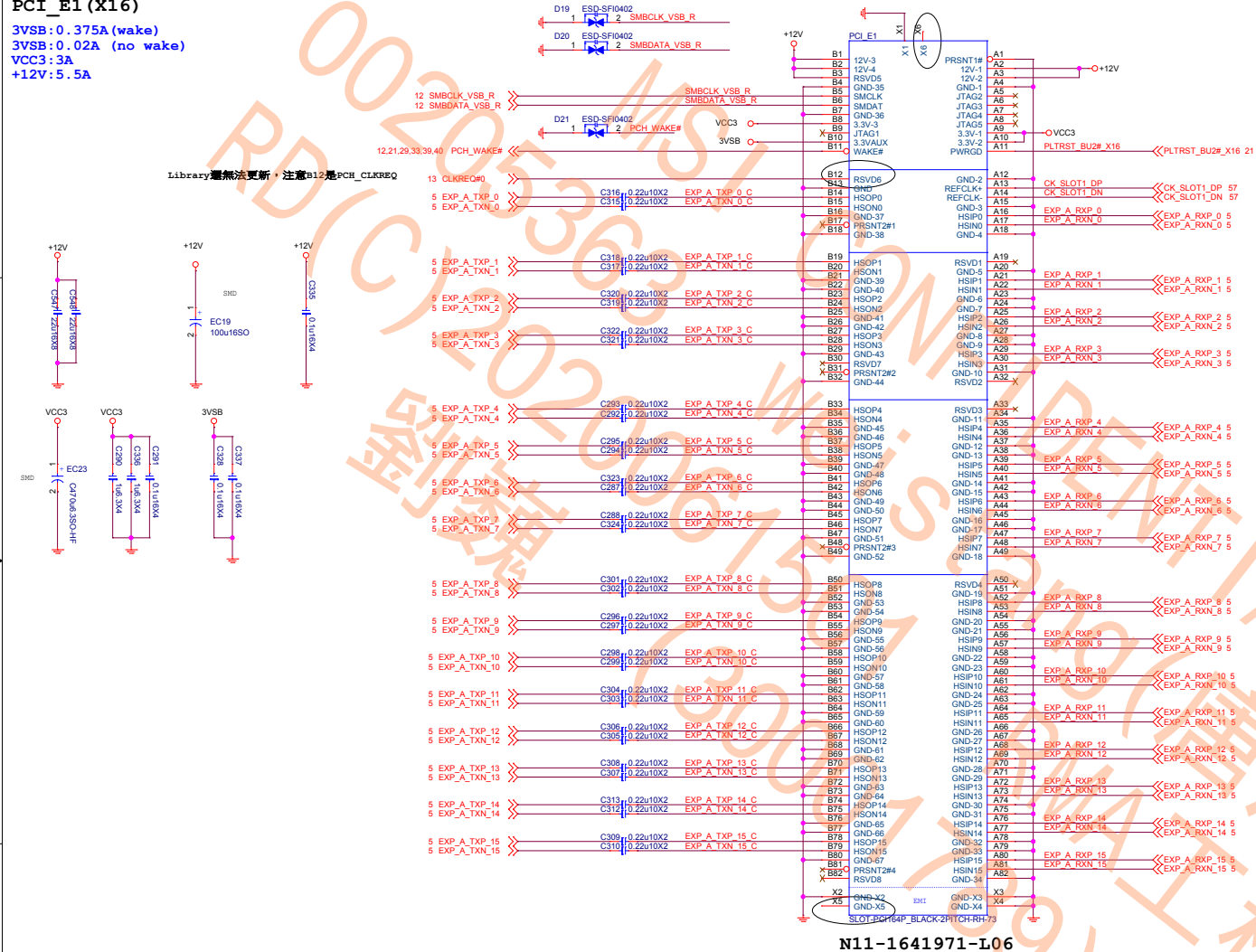


MICRO-STAR INT'L CO.,LTD

MS-7C77

Size Custom Document Description 18 PCH-Strap Rev 1.1  
Date: Friday, January 31, 2020 Sheet 18 of 63

```
3VSB:0.375A(wake)
3VSB:0.02A (no wake)
VCC3:3A
+12V:5.5A
```



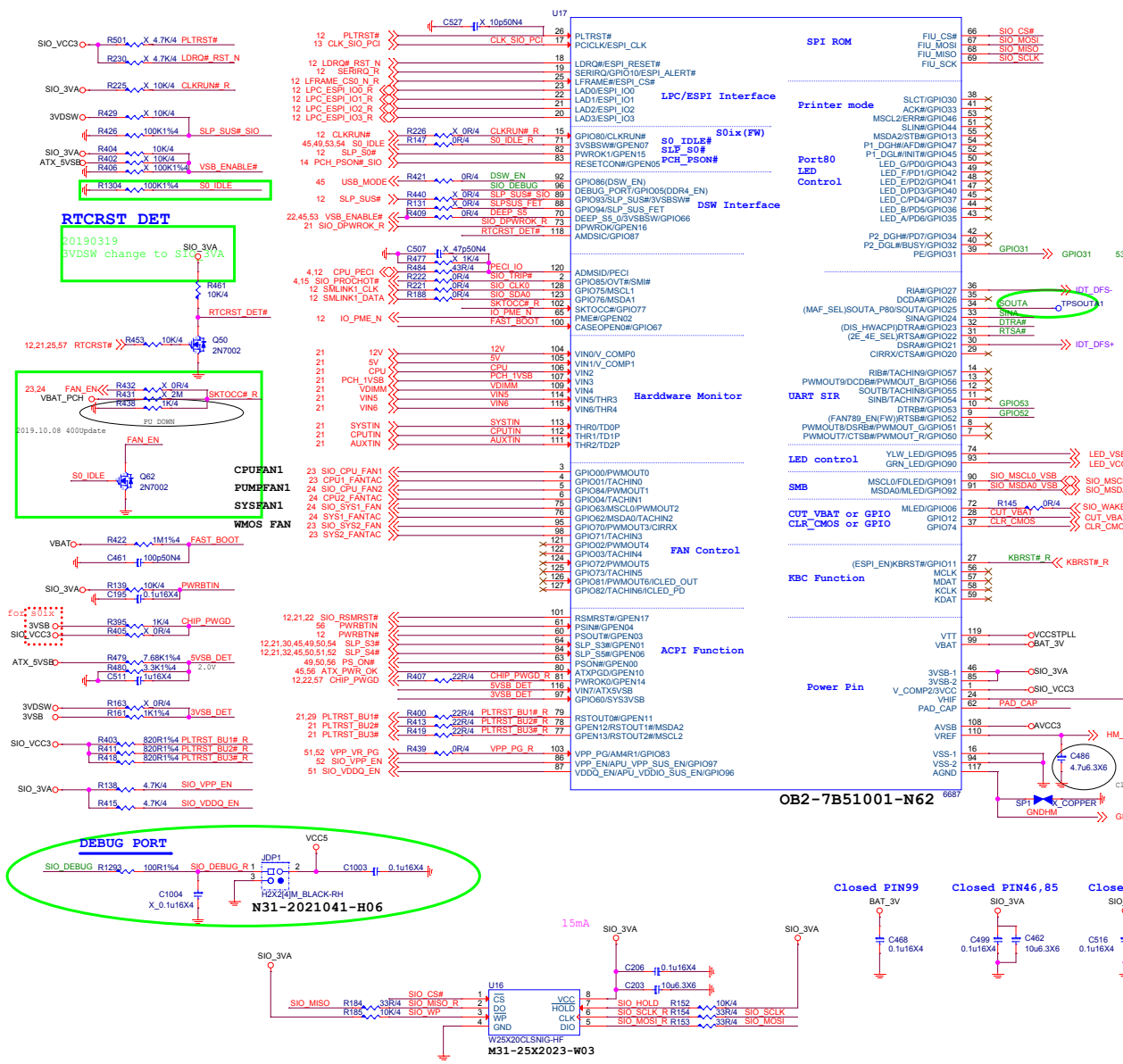
N11-1641971-L06



MS-7C77

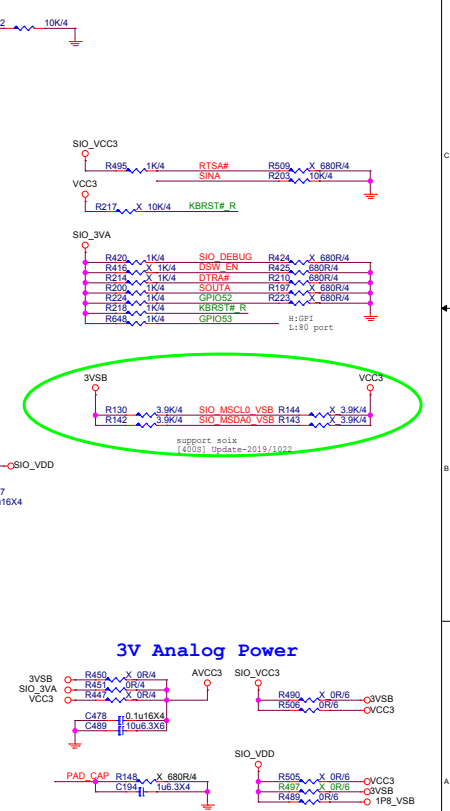
Rev	1.1
-----	-----

Date: Friday, January 31, 2020

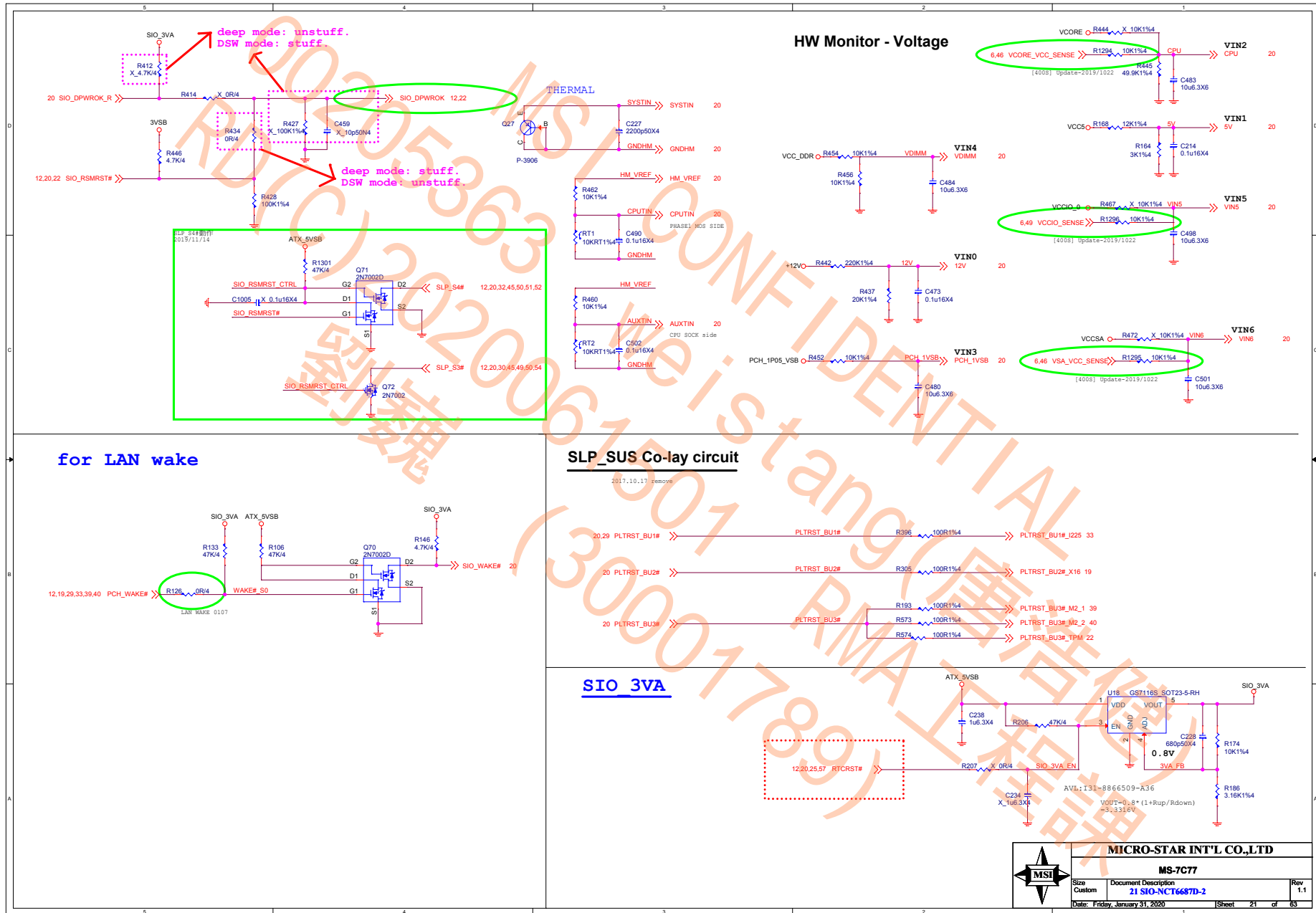


POWER ON STRAPPING PIN FOR NCT6687

PIN	NAME	Circuit NAME	0	1	Strap Point
31	2E_4E_SEL	RTSA#	I/O ADDRESS 2E	I/O ADDRESS 4E	3VCC
32	DIS_HWACPI	DTRA#	HW ACPI enable	HW ACPI disable	3VA
34	MAF_SEL	SOUTA	MAF enable	MAF disable	3VA
92	DSW_EN	DSW_EN	DSW enable	DSW disable	3VA
96	DDR4_EN	SIO_DEBUG	DDR4 control disable	DDR4 control enable	3VA
9	FAN789_EN (FW setting)	GPIO52	FAN789 disable	FAN789 enable	3VA
PIN	NAME	Circuit NAME	VCC3	3VA	Strap Point
27	ESPI_EN	KBRST#	LPC	ESPI	VCC3 or 3VA

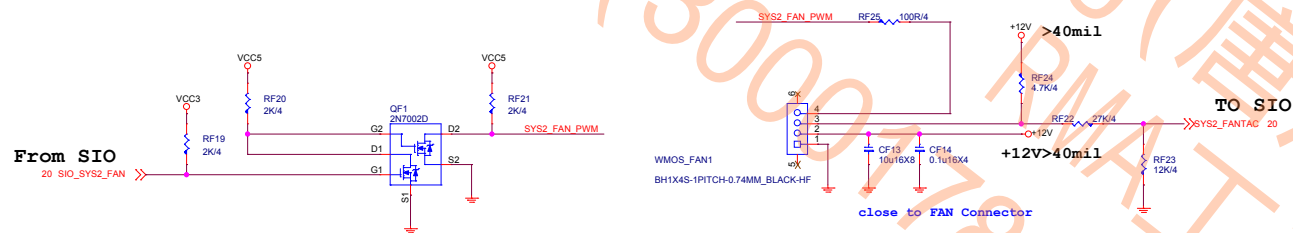






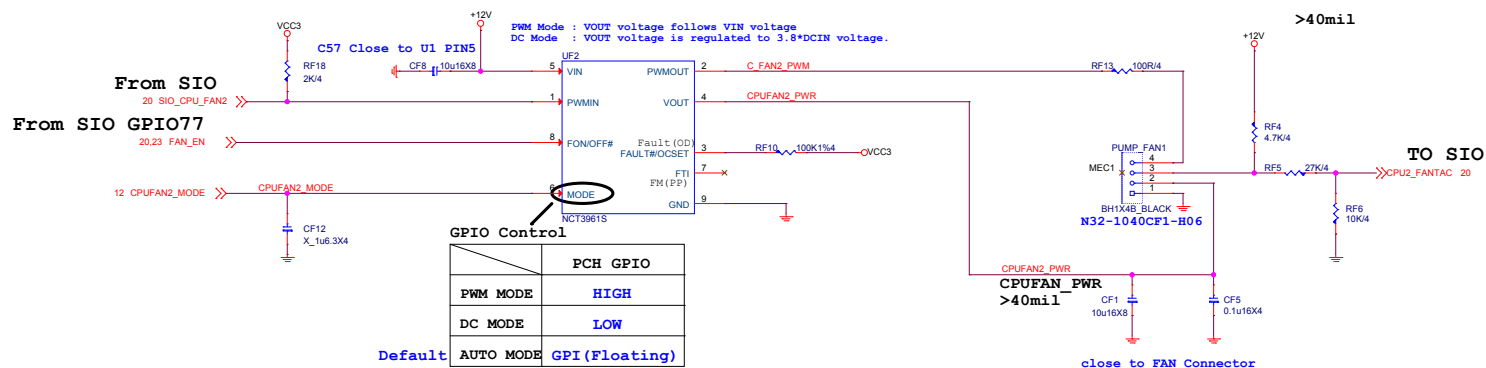
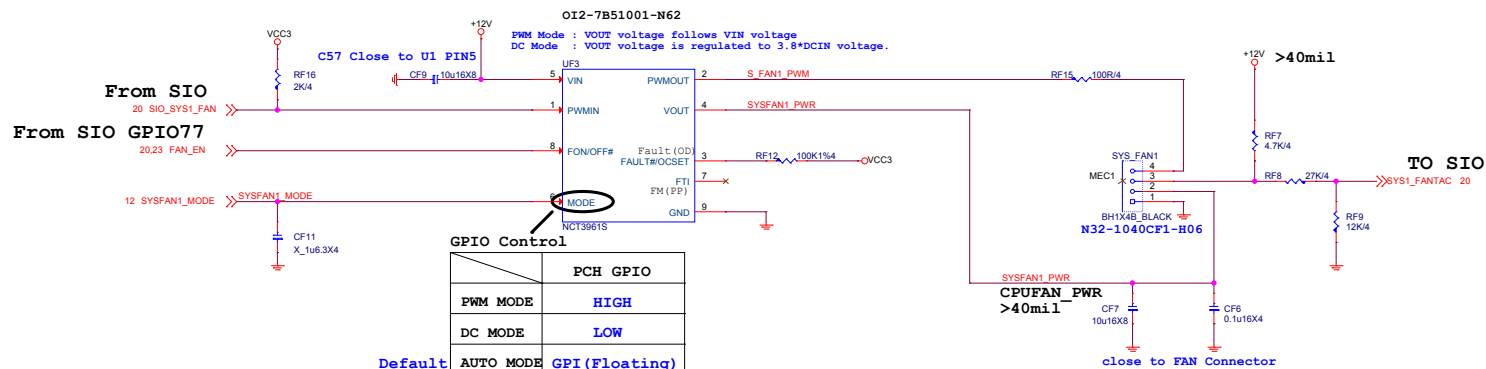


## 2.FM:BIOS can read FAN PWM/DC MODE



# TYPE M : 4 PIN CPU FAN USE NCT3961S USE PCH GPIO CONTROL FAN MODE

1.Mode GPIO BIOS can switch PWM/DC MODE

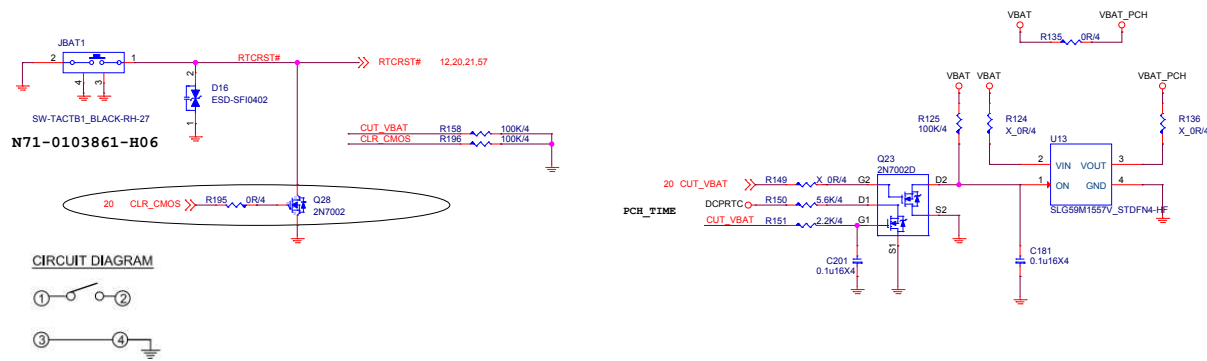


MICRO-STAR INT'L CO.,LTD

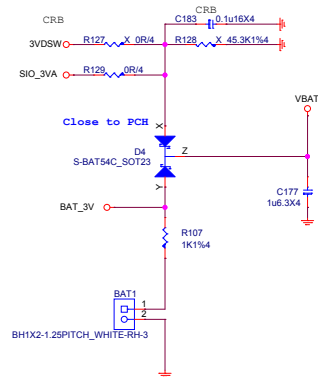
MS-7C77

Size Custom Document Description 24 FAN PUMP/SYS M Rev 1.1  
Date: Friday, January 31, 2020 Sheet 24 of 63

## CLR COMS

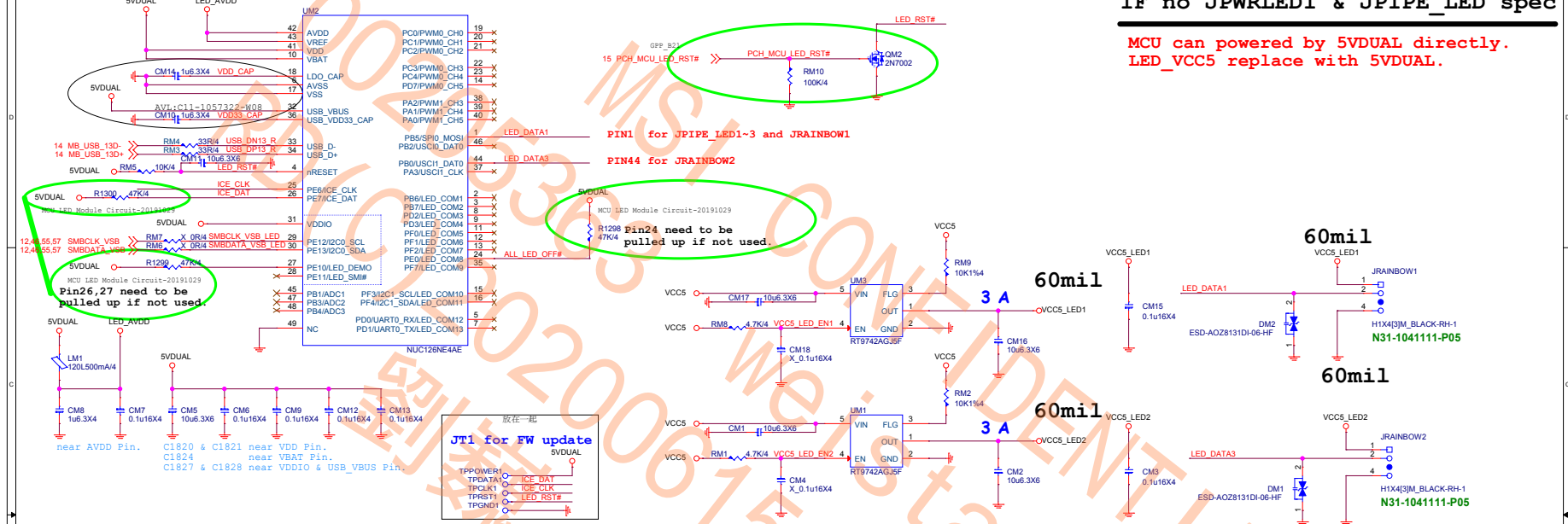


## VBAT

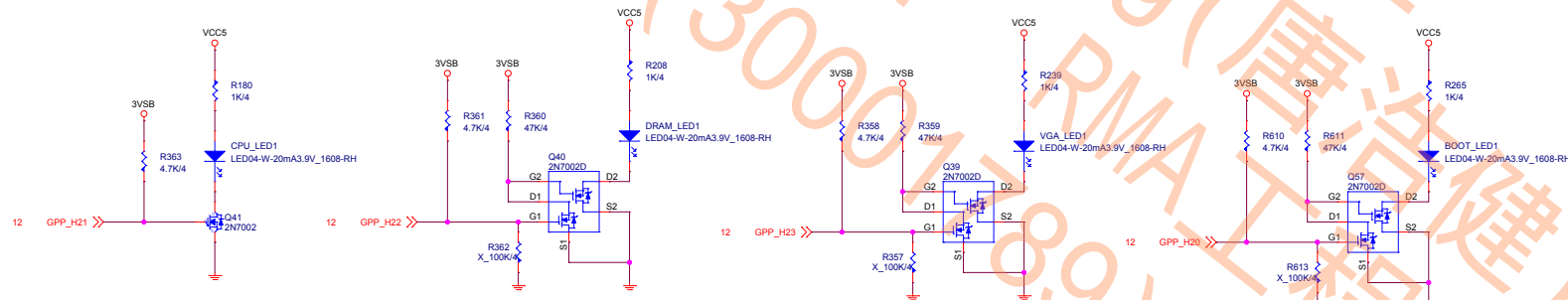


IF no JPWRLED1 & JPIPE\_LED spec

MCU can powered by 5VDUAL directly.  
LED\_VCC5 replace with 5VDUAL.



## DEBUG LED



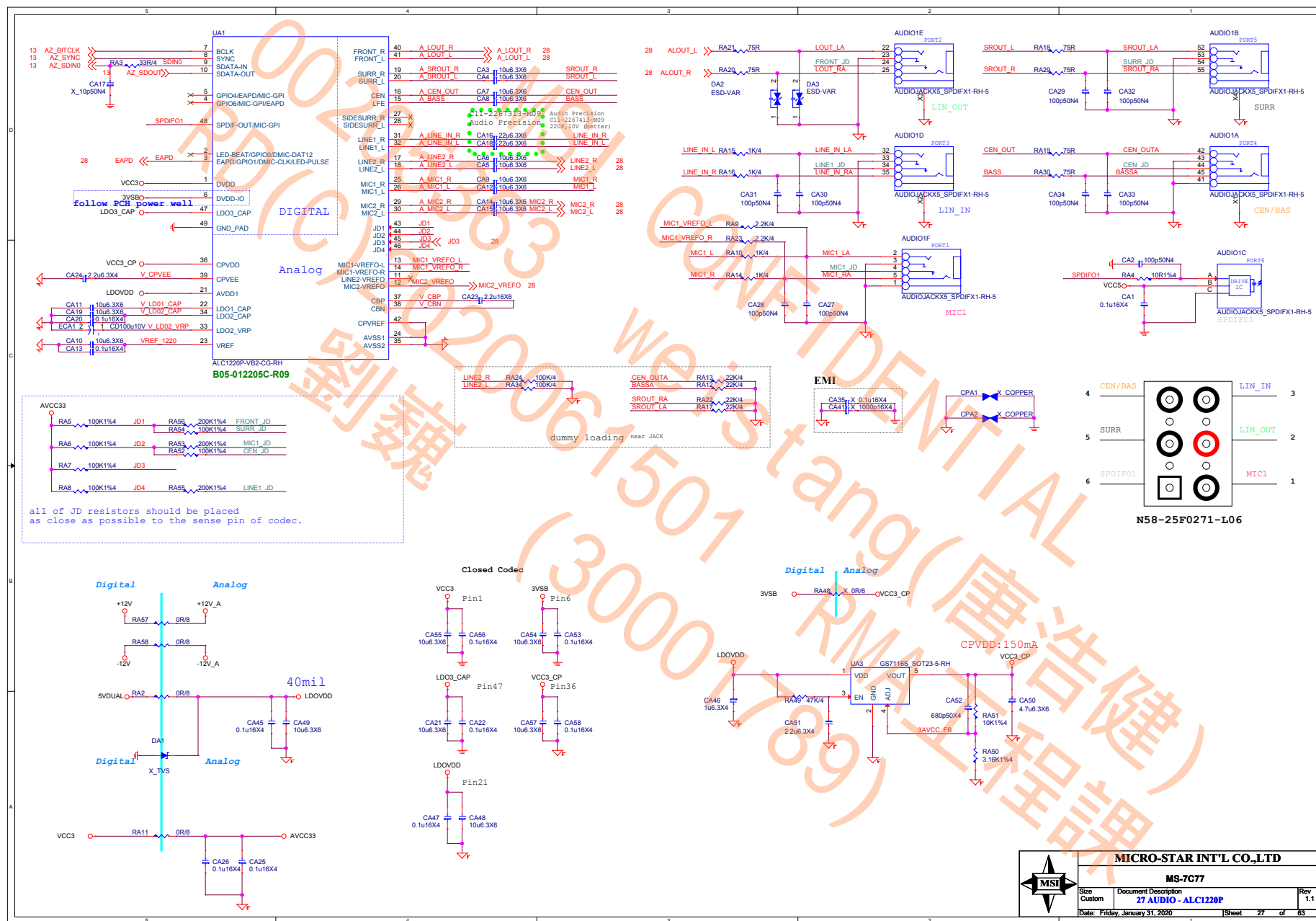
LED	PCH_GP20	PCH_GP21	PCH_GP22	PCH_GP23
亮	NATIVE PULL HIGH	GPO PULL HIGH	GPO PULL HIGH	NATIVE PULL HIGH
滅	NATIVE LOW	GPO LOW (default LOW)	GPO LOW (default LOW)	GPO LOW (default LOW)

開機斷電狀態下，4個LED先維持default全暗，開機通電後：

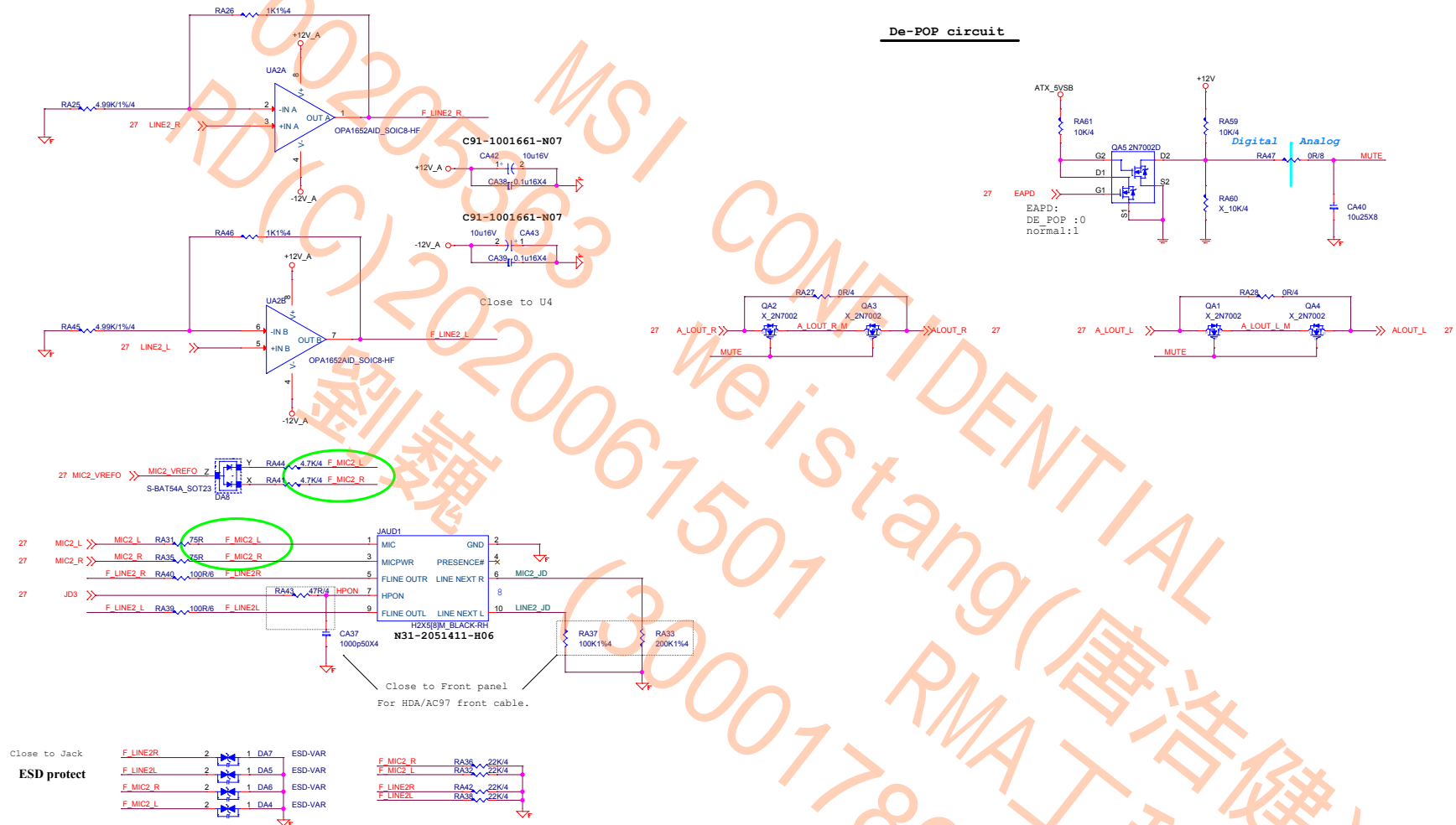
1. 首先進行CPU checkCPU LED 亮，check PASS後則CPU LED減掉。
2. 接著依序進行Memory /memory LED亮check PASS後則memory LED減掉。
3. VGA的check/VGA LED亮，check PASS後則VGA LED減掉。
4. 因此最後正常順利開機後，三個LED燈都是減掉的。

(系統重啟或其他原因造成系統重開機，則LED仍按上述行為動作)



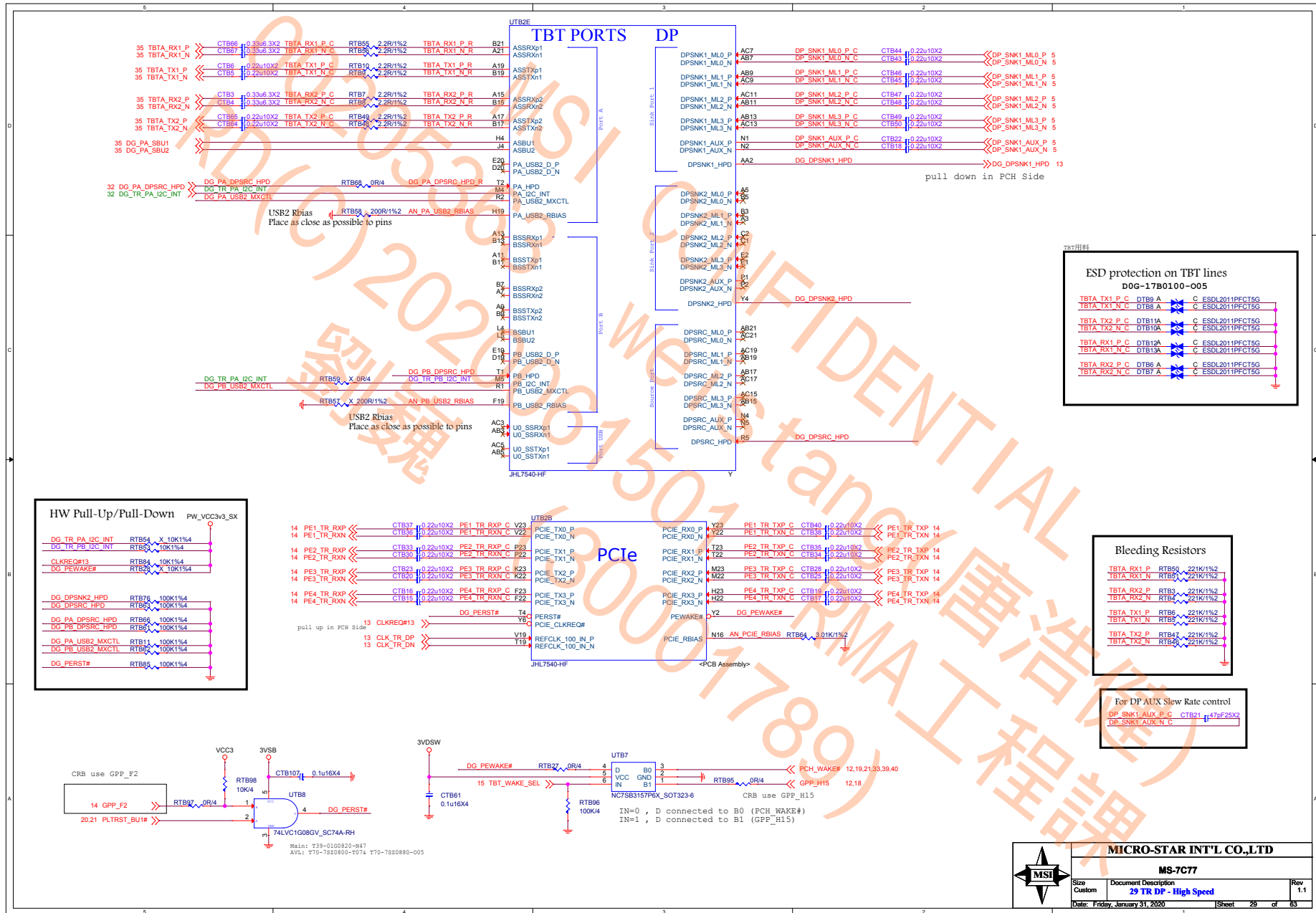


# De-POP circuit

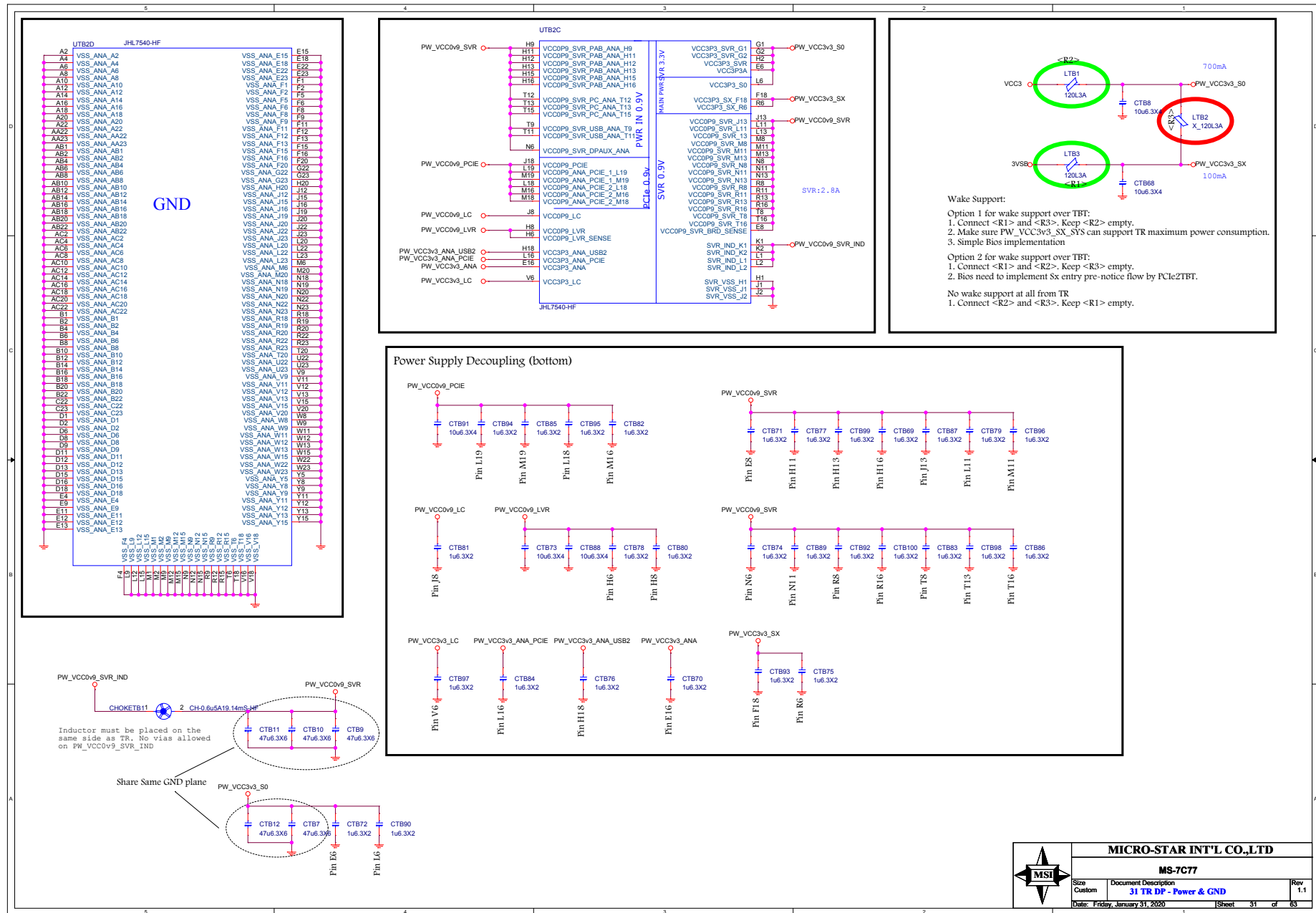


Close to Jack  
ESD protect

Close to Front panel  
For HDA/AC97 front cable.











## SMBUS Address 0x49



**MICRO-STAR INT'L CO.,LTD**

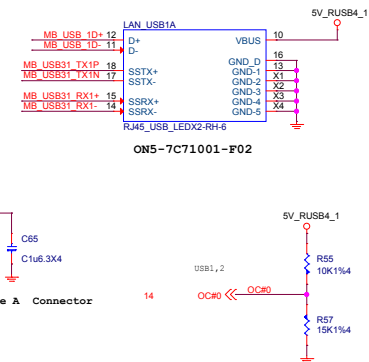
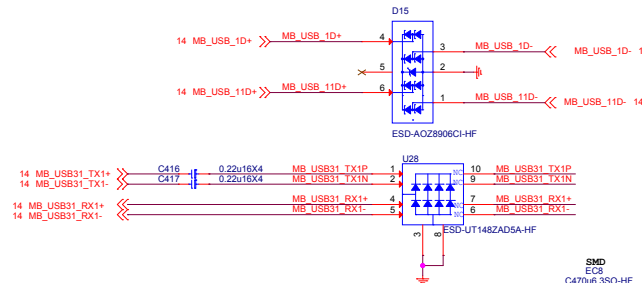
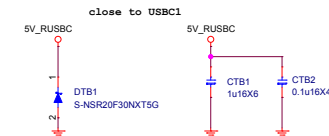
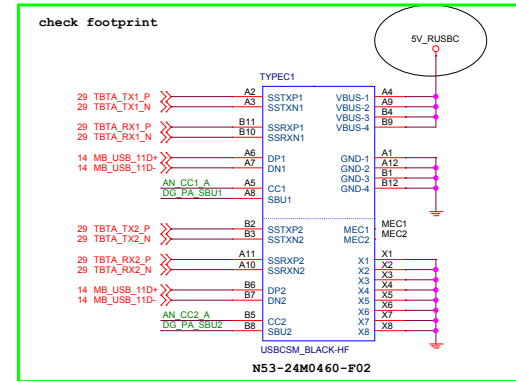
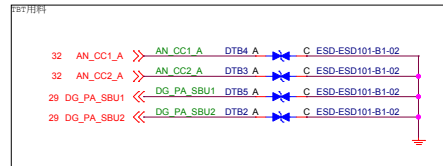
MS-7C77

Size Custom	Document Description <b>33 LAN - INTEL I255V</b>
----------------	---

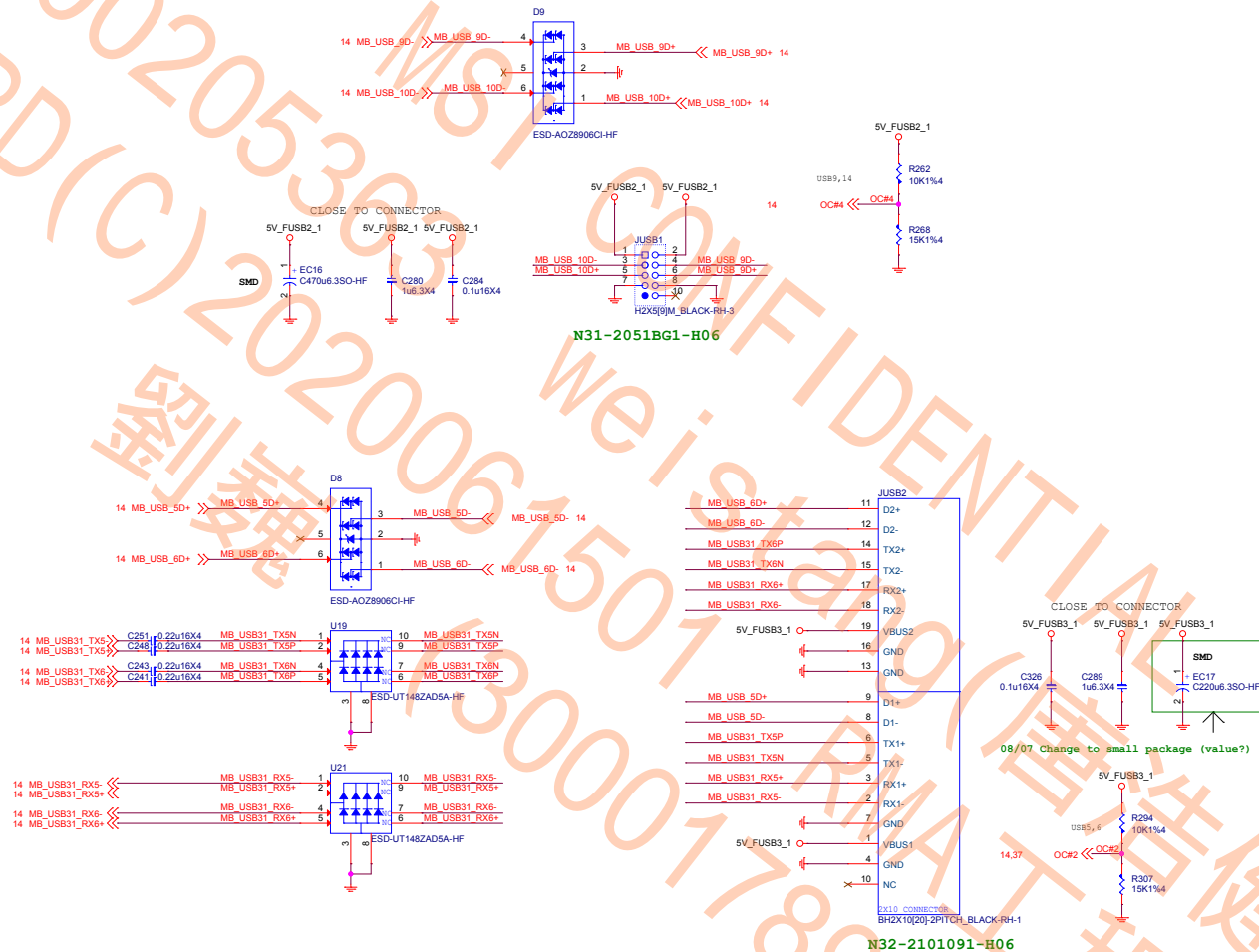
Date: Friday, January 31, 2020

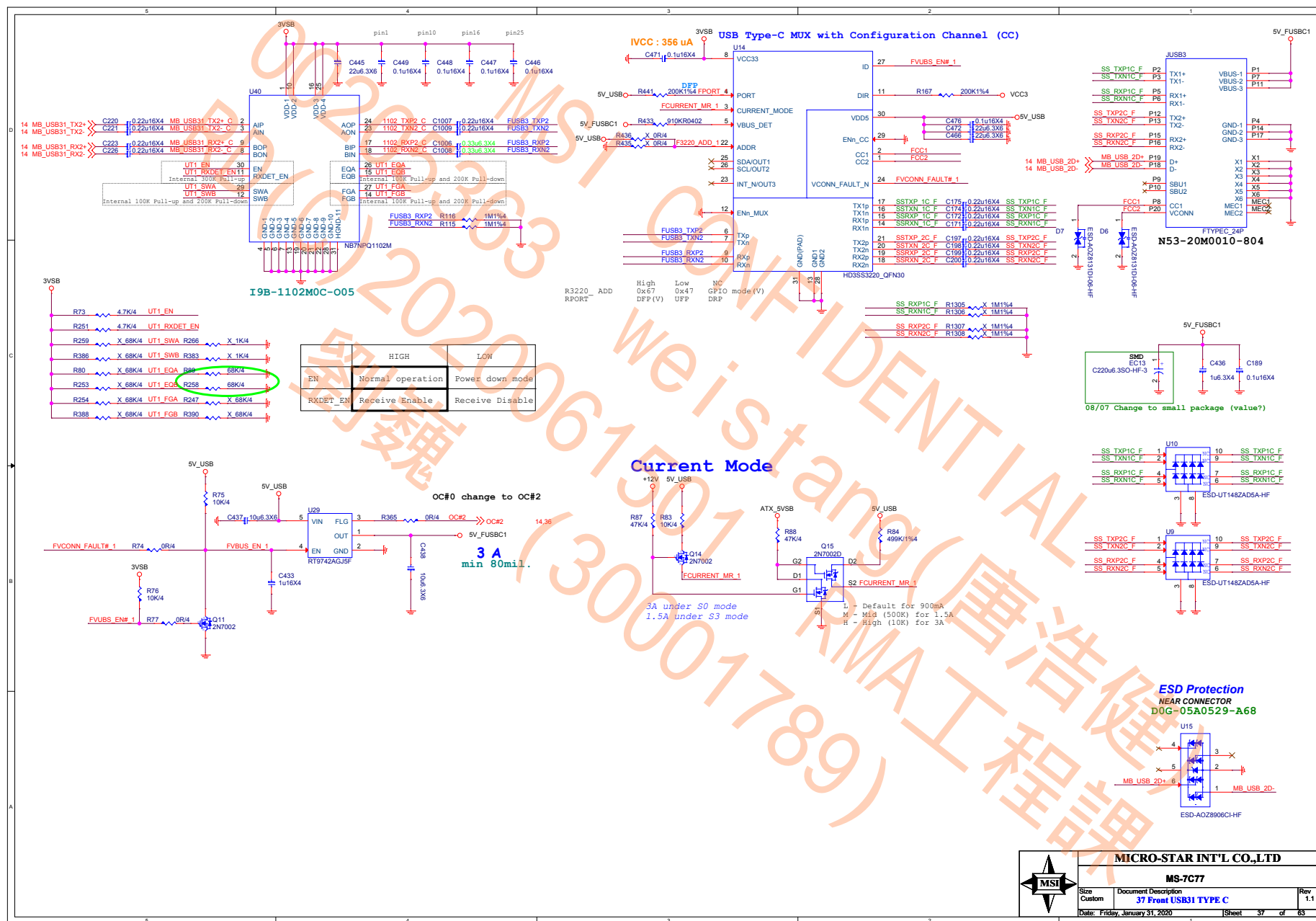
Rev	1.1
63	





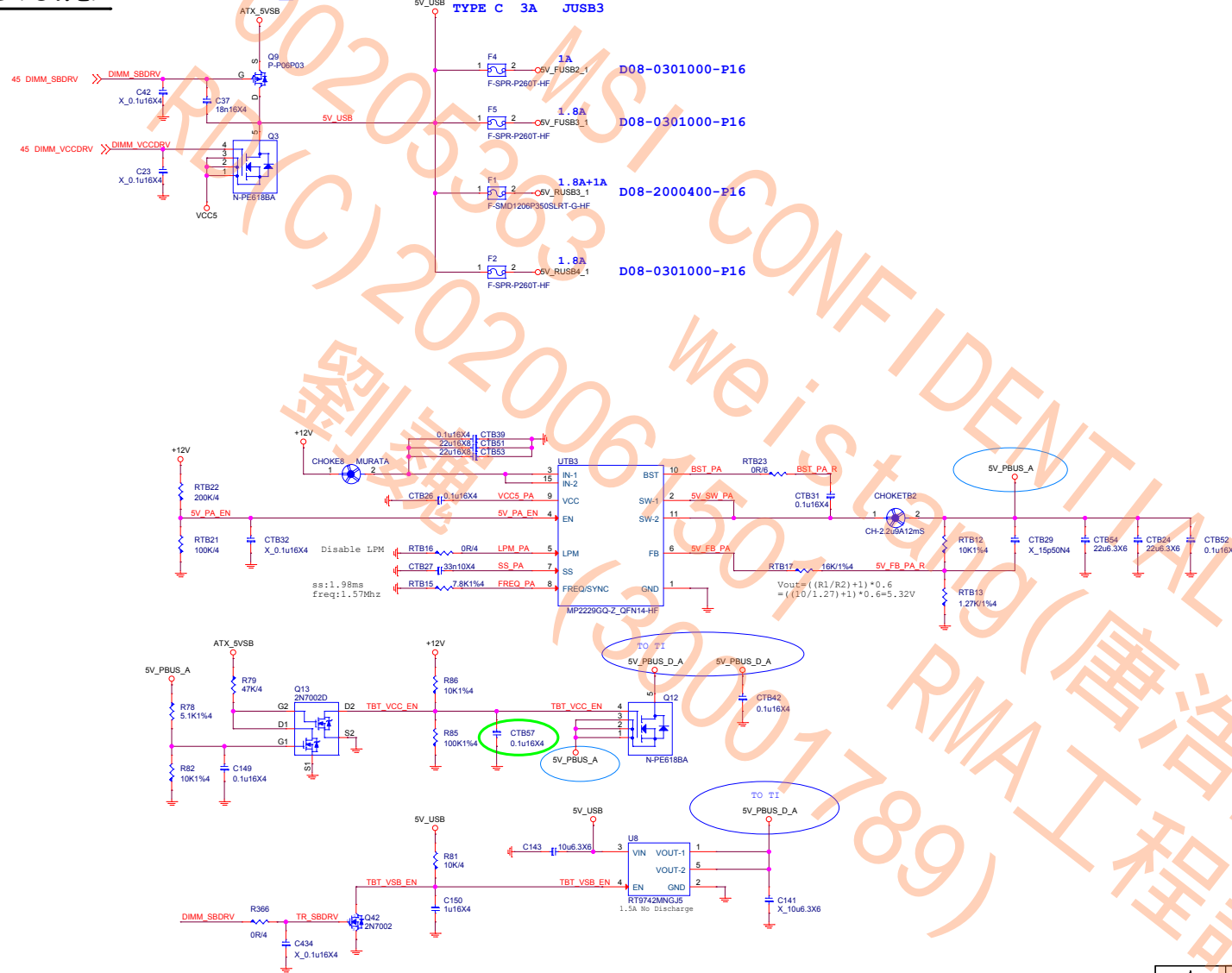
00205330 (MSI) CONFIDENTIAL  
RD(C) 20200615017501789 (MSI) 工程師 (Wei's)

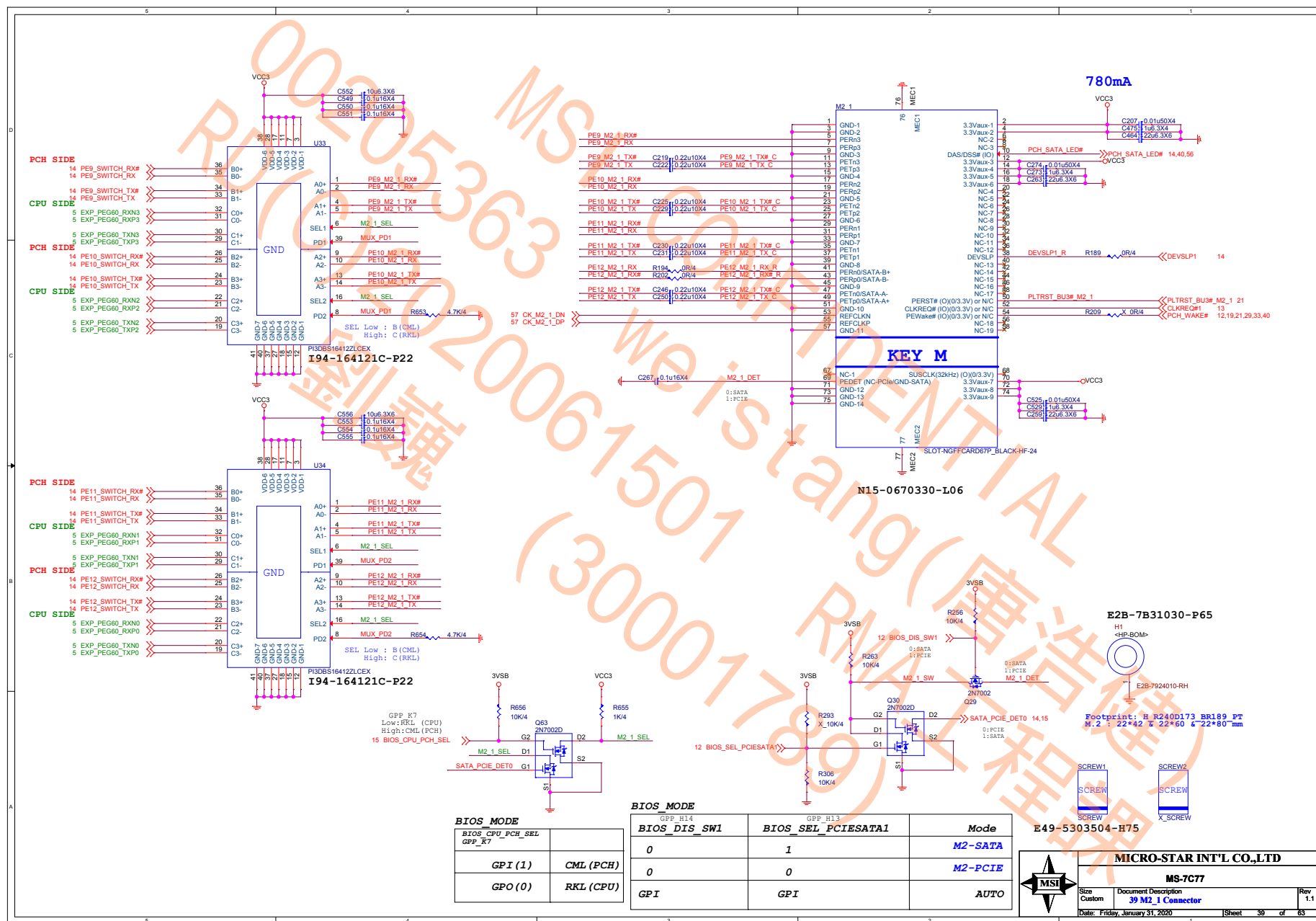




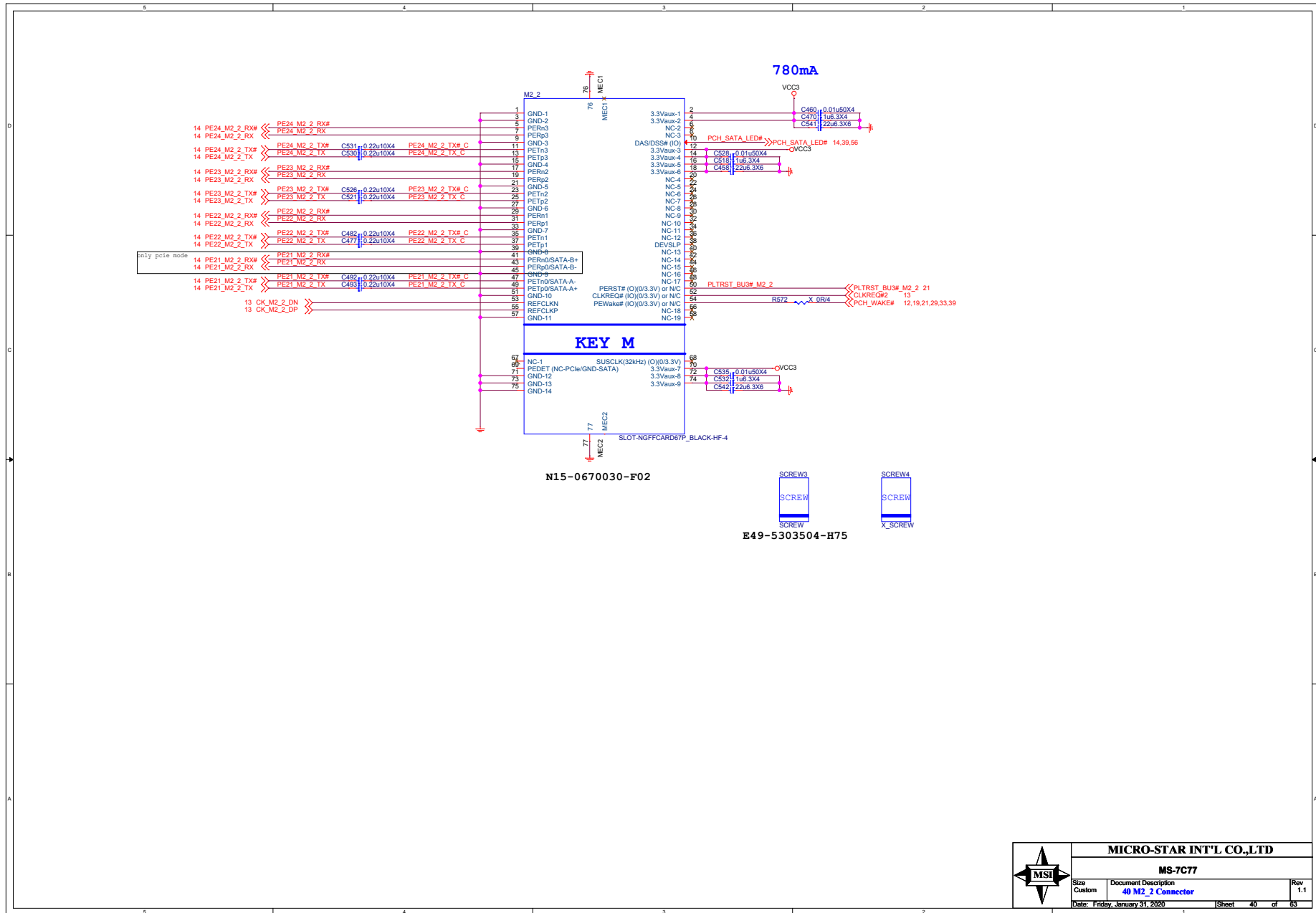
# USB POWER

5V\_USB:10.4A

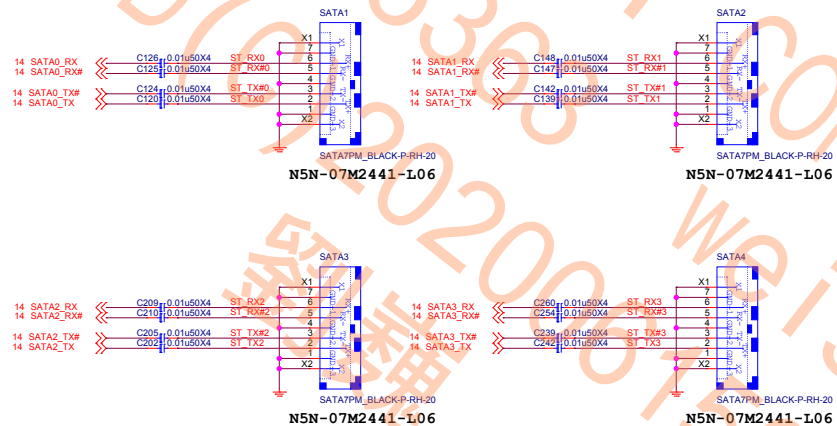




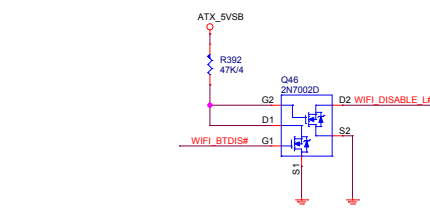
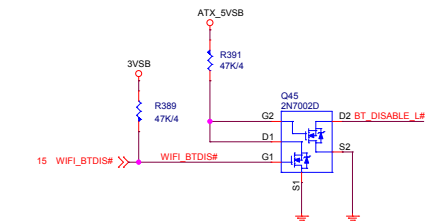
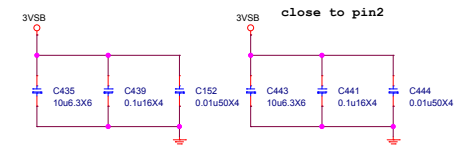
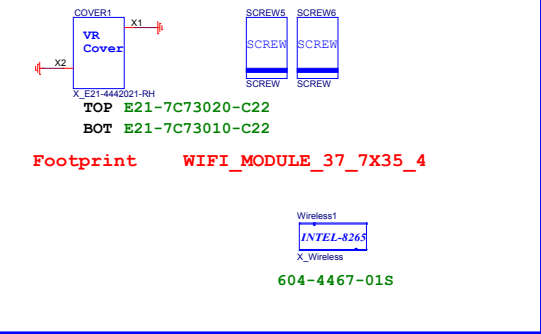
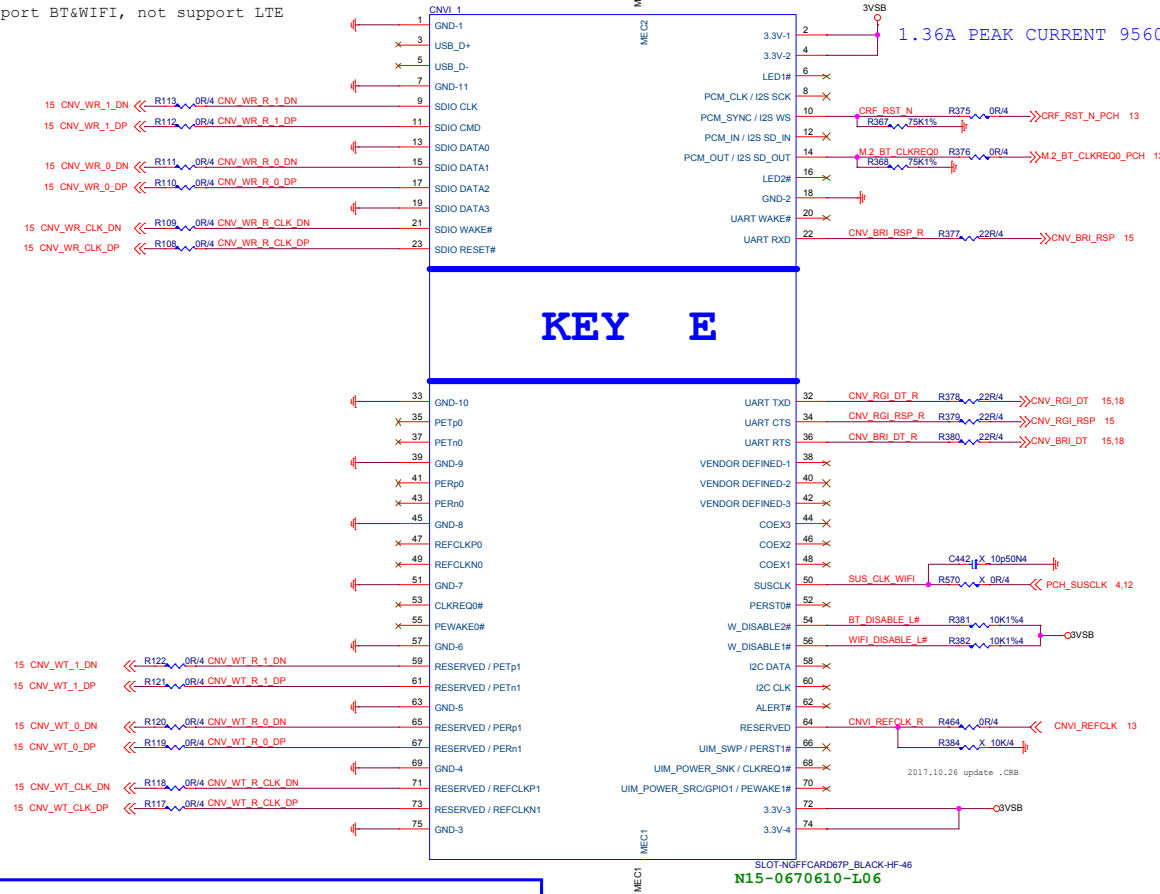




# SATA GEN3

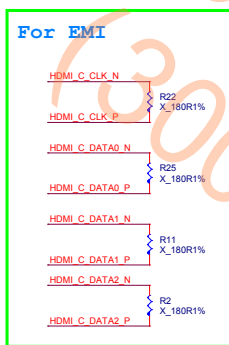
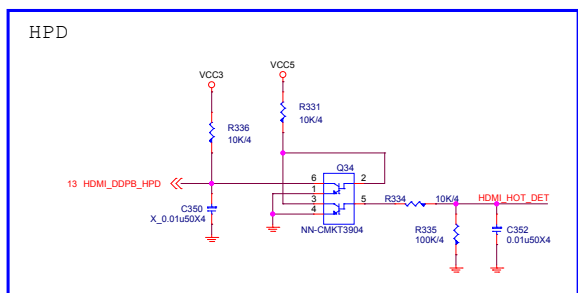
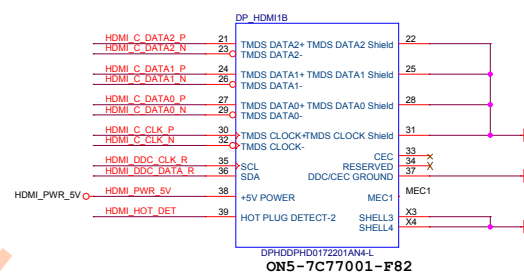
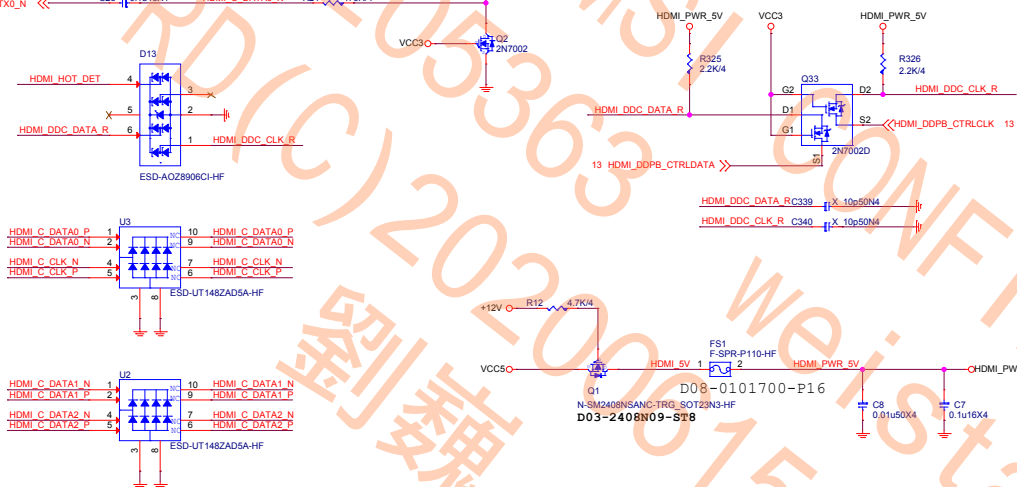


support BT&WIFI, not support LTE





5	HDMI_D0P6_CLK_P	C17	0.1u16K4	HDMI_C_CLK_P	R19	470R4	HDMI_MOS_DATA
5 <th>HDMI_D0P6_CLK_N</th> <th>C18</th> <th>0.1u16K4</th> <th>HDMI_C_CLK_N</th> <th>R21</th> <th>470R4</th> <td></td>	HDMI_D0P6_CLK_N	C18	0.1u16K4	HDMI_C_CLK_N	R21	470R4	
5 <th>HDMI_D0P6_T02_P</th> <th>C19</th> <th>0.1u16K4</th> <th>HDMI_C_DATA2_P</th> <th>R1</th> <th>470R4</th> <td></td>	HDMI_D0P6_T02_P	C19	0.1u16K4	HDMI_C_DATA2_P	R1	470R4	
5 <th>HDMI_D0P6_T02_N</th> <th>C18</th> <th>0.1u16K4</th> <th>HDMI_C_DATA2_N</th> <th>R4</th> <th>470R4</th> <td></td>	HDMI_D0P6_T02_N	C18	0.1u16K4	HDMI_C_DATA2_N	R4	470R4	
5 <th>HDMI_D0P6_T01_P</th> <th>C8</th> <th>0.1u16K4</th> <th>HDMI_C_DATA1_P</th> <th>R7</th> <th>470R4</th> <td></td>	HDMI_D0P6_T01_P	C8	0.1u16K4	HDMI_C_DATA1_P	R7	470R4	
5 <th>HDMI_D0P6_T01_N</th> <th>C9</th> <th>0.1u16K4</th> <th>HDMI_C_DATA1_N</th> <th>R14</th> <th>470R4</th> <td></td>	HDMI_D0P6_T01_N	C9	0.1u16K4	HDMI_C_DATA1_N	R14	470R4	
5 <th>HDMI_D0P6_T00_P</th> <th>C26</th> <th>0.1u16K4</th> <th>HDMI_C_DATA0_P</th> <th>R27</th> <th>470R4</th> <td></td>	HDMI_D0P6_T00_P	C26	0.1u16K4	HDMI_C_DATA0_P	R27	470R4	
5 <th>HDMI_D0P6_T00_N</th> <th>C25</th> <th>0.1u16K4</th> <th>HDMI_C_DATA0_N</th> <th>R24</th> <th>470R4</th> <td></td>	HDMI_D0P6_T00_N	C25	0.1u16K4	HDMI_C_DATA0_N	R24	470R4	

[illegible]

MS-7C77

	Rev 1.1
--	------------

Sheet 44 of 63

The schematic diagram illustrates the S0IX board's internal circuitry. At the center is the **uP5701** microcontroller, which is connected to several key components:

- Power Management:** The **5VDC\_DRV** pin of the uP5701 is connected to a 5VDC supply. The **3VDSW** pin is connected to a 3VDSW supply. The **ATX\_5VSB** pin is connected to an ATX\_5VSB supply.
- Memory:** The **SLP\_S3#\_EVDUAL** pin is connected to a **SLP\_S3#\_EVDUAL** signal. The **SLP\_S3#\_S4#** pin is connected to a **SLP\_S3#\_S4#** signal. The **SLP\_S3#\_S5#** pin is connected to a **SLP\_S3#\_S5#** signal.
- IO and Control:** The **MODE** pin is connected to a **MODE** signal. The **SLP\_S3#\_S6#** pin is connected to a **SLP\_S3#\_S6#** signal. The **SLP\_S3#\_S7#** pin is connected to a **SLP\_S3#\_S7#** signal.
- Other Components:** The **SLP\_S3#\_S8#** pin is connected to a **SLP\_S3#\_S8#** signal. The **SLP\_S3#\_S9#** pin is connected to a **SLP\_S3#\_S9#** signal. The **SLP\_S3#\_S10#** pin is connected to a **SLP\_S3#\_S10#** signal.

The diagram also shows various other components such as resistors (R3, R4, R5, R6, R7, R8, R9, R10, R11, R12, R13, R14, R15, R16, R17, R18, R19, R20, R21, R22, R23, R24, R25, R26, R27, R28, R29, R30, R31, R32, R33, R34, R35, R36, R37, R38, R39, R40, R41, R42, R43, R44, R45, R46, R47, R48, R49, R50, R51, R52, R53, R54, R55, R56, R57, R58, R59, R60, R61, R62, R63, R64, R65, R66, R67, R68, R69, R70, R71, R72, R73, R74, R75, R76, R77, R78, R79, R80, R81, R82, R83, R84, R85, R86, R87, R88, R89, R90, R91, R92, R93, R94, R95, R96, R97, R98, R99, R100), capacitors (C1, C2, C3, C4, C5, C6, C7, C8, C9, C10, C11, C12, C13, C14, C15, C16, C17, C18, C19, C20, C21, C22, C23, C24, C25, C26, C27, C28, C29, C30, C31, C32, C33, C34, C35, C36, C37, C38, C39, C40, C41, C42, C43, C44, C45, C46, C47, C48, C49, C50, C51, C52, C53, C54, C55, C56, C57, C58, C59, C60, C61, C62, C63, C64, C65, C66, C67, C68, C69, C70, C71, C72, C73, C74, C75, C76, C77, C78, C79, C80, C81, C82, C83, C84, C85, C86, C87, C88, C89, C90, C91, C92, C93, C94, C95, C96, C97, C98, C99, C100), and other integrated circuits (ICs) like the **Q35** and **Q36** transistors.

**7501 Mode**  
H: Support S0/S3/S5  
L: Support S0/S3

For S5-G3 can ramp issue

[illegible]

2.64A

3VSB 3VSB

3VSB\_EN

3VSB\_PWRGD

3VSB\_VCC

3VSB\_MODE

MODE Sourcing Current: 5A

TPS566235

1.95-566230C-707 6A

MODE:

ECO: 0-0.3V (80)

COA: 0.3-1.2V (100K-150K)

FOCI: >1.2V (>400K)

ENABLE VIH: 1.1-1.4V

5VDUAL

5VSB

ATX\_5VSB

R123 47K/4

20.2253 VSB\_ENABLE#

SIO\_3VA

R155 0R/4

Q21 2N7002D

G2

D2

3VSB\_EN

D1

G1

S2

3VSB\_EN

R140 100K1/4

R134 82K1/4

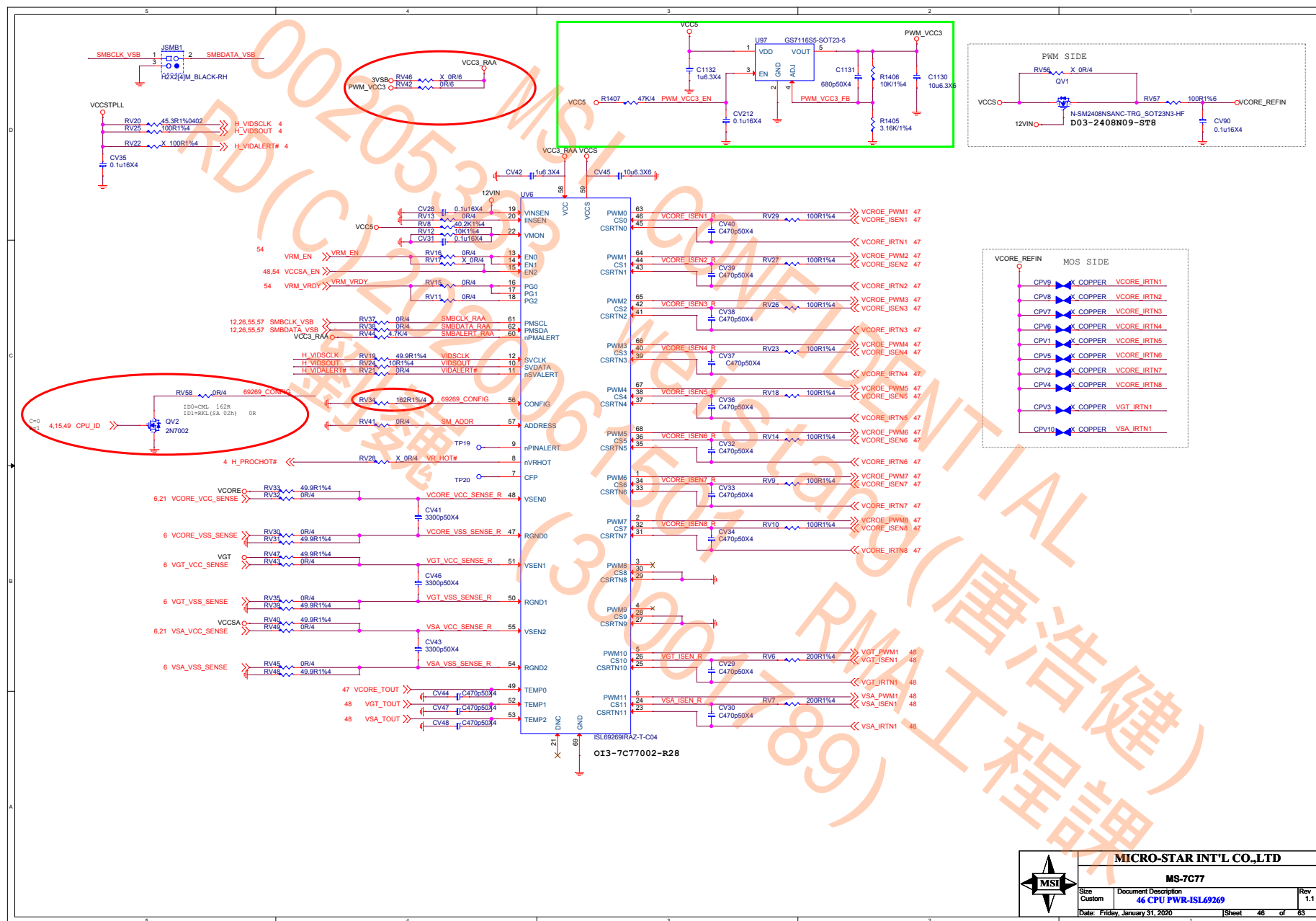
C187 1uF 3X4

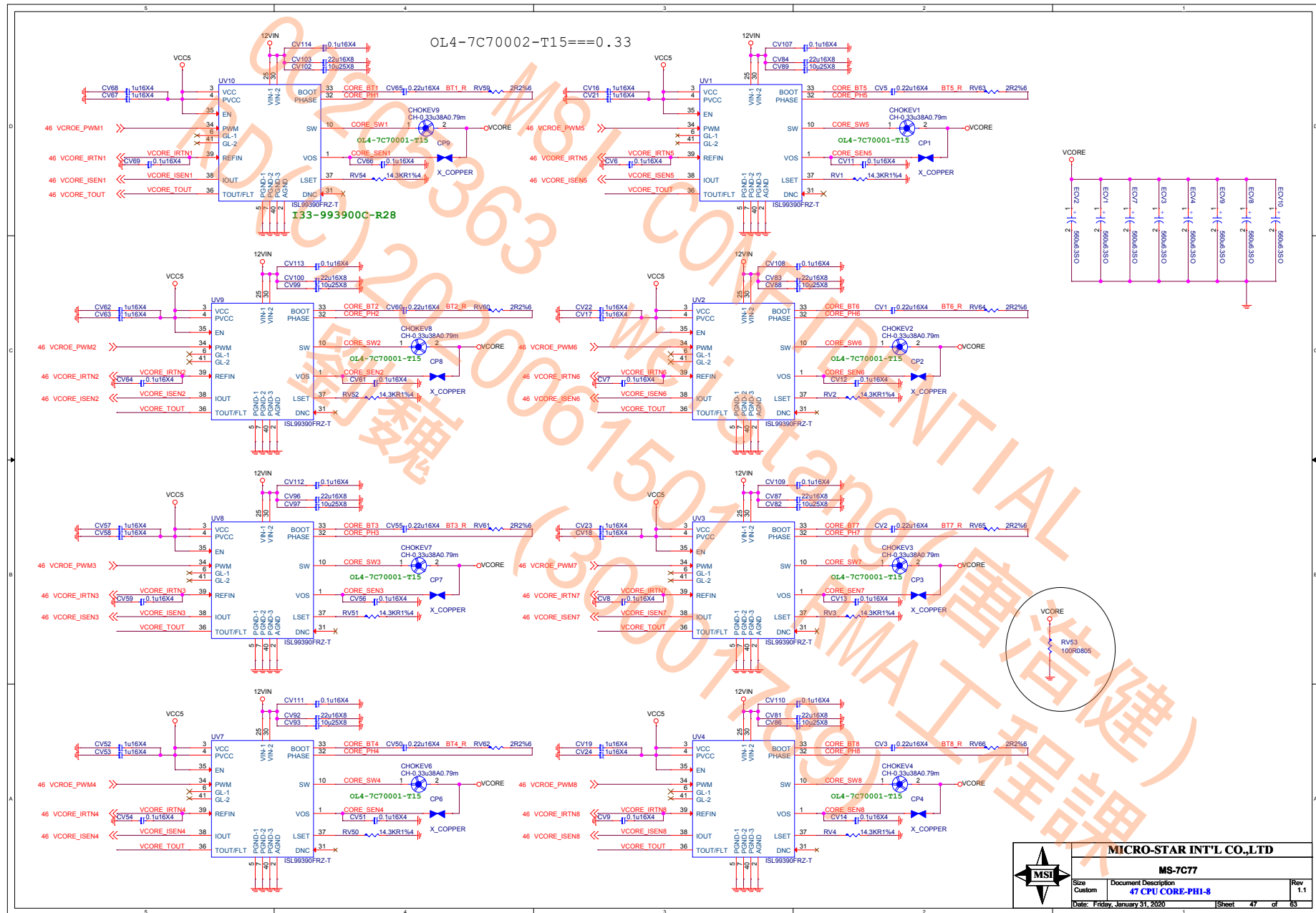
Q25 2N3904

For S5-G3 3VSB EN issue



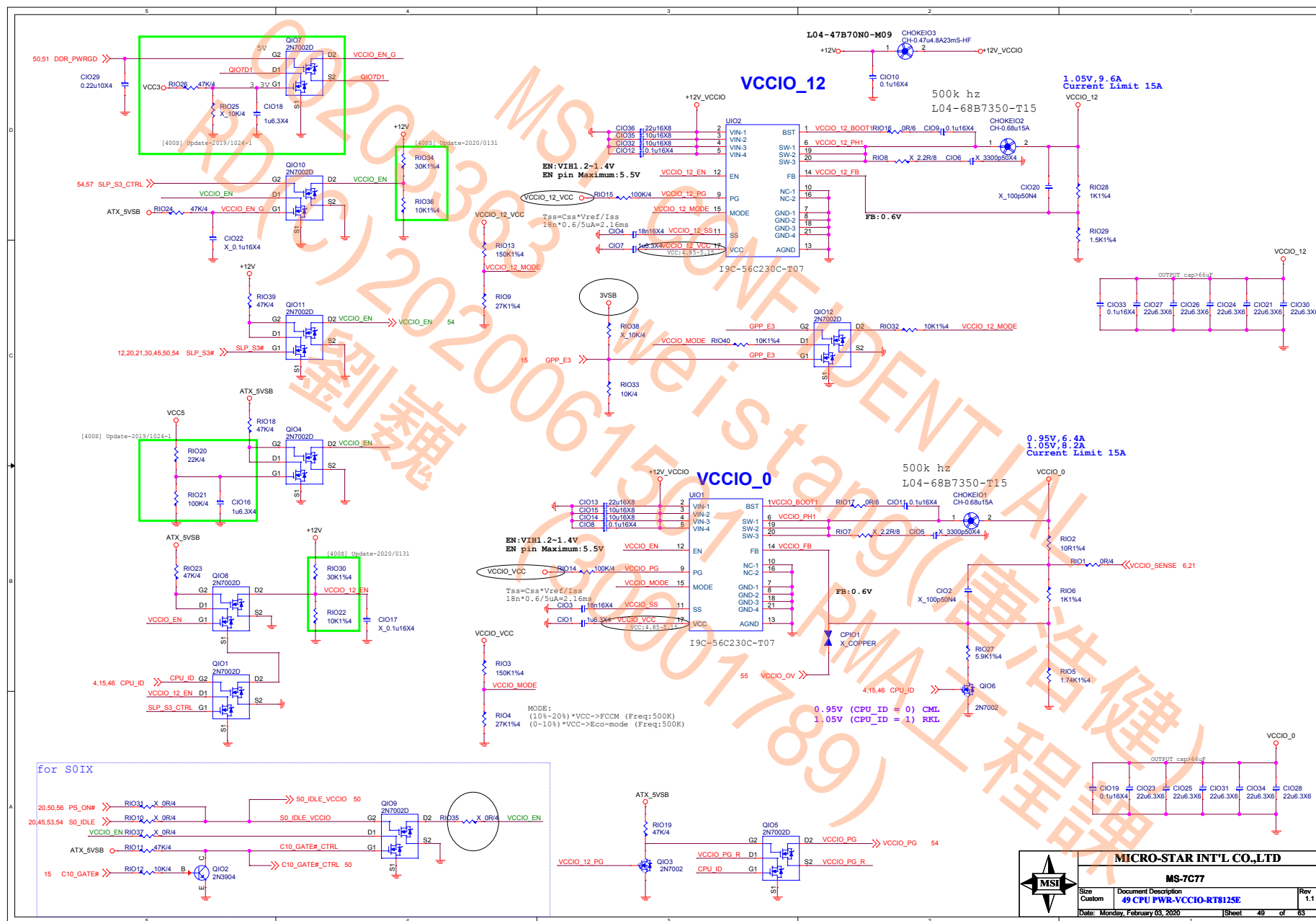
Date: Friday, January 31, 2020 Sheet 45 of 63

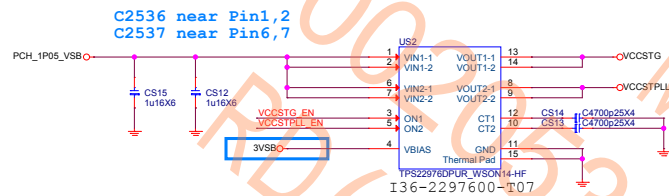








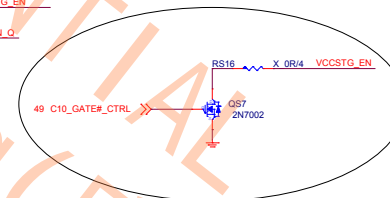
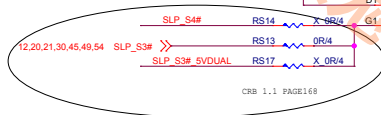
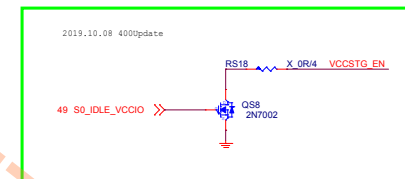
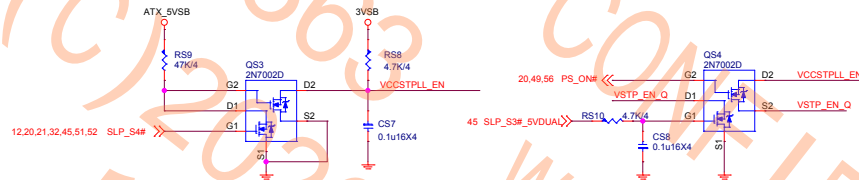
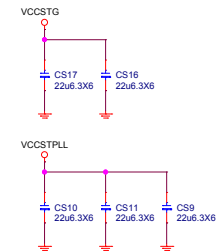




**VCCSTG**  
1.05V; 0.2A/0.9A

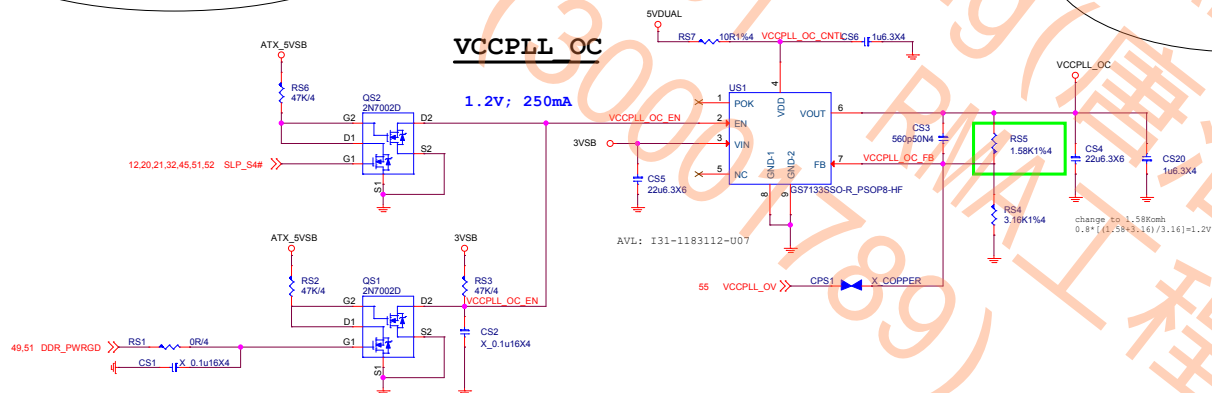
**VCCSTPLL**  
1.05V; 0.92A/2.3A+0.23A=2.53A

VCCST VCCPLL



**VCCPLL OC**

1.2V; 250mA



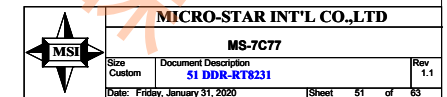
MICRO-STAR INT'L CO.,LTD

MS-7C77

Size  
Custom Document Description  
50 CPU PWR-VCCST/PLL  
Rev  
1.1  
Date: Friday, January 31, 2020 1 Sheet 50 of 63

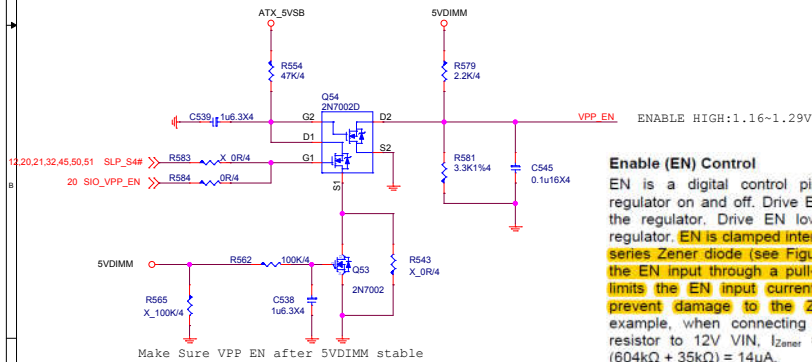
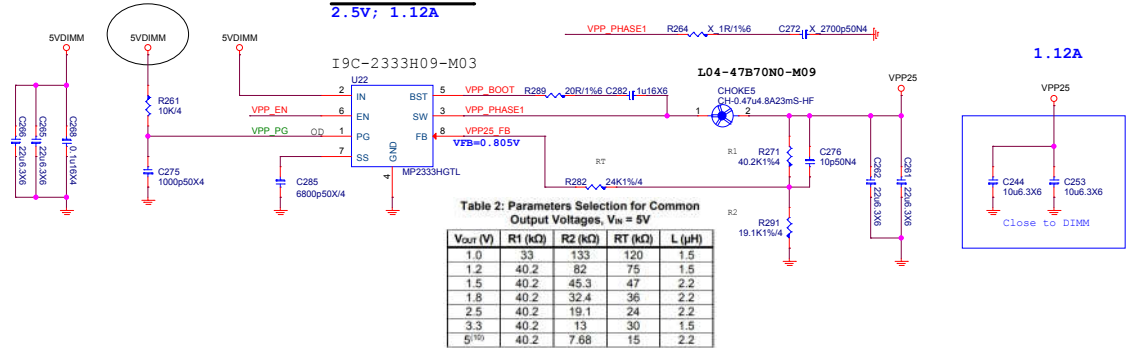
$$3.3A + 5.85A + 0.375A = 9.525A$$

$$3.3A + 5.85A + 0.375A = 9.525A$$



## VPP Power:2.5V; 1.12A FOR 2DIMM

### VPP25 Power 2.5V; 1.12A



### Enable (EN) Control

EN is a digital control pin that turns the regulator on and off. Drive EN high to turn on the regulator. Drive EN low to turn off the regulator. EN is clamped internally using a 2.8V series Zener diode (see Figure 2). Connecting the EN input through a pull-up resistor to  $V_{IN}$  limits the EN input current below 40 $\mu$ A to prevent damage to the Zener diode. For example, when connecting a 604k $\Omega$  pull-up resistor to 12V  $V_{IN}$ ,  $I_{Zener} = (12V - 2.8V) / (604k\Omega + 35k\Omega) = 14\mu A$ .

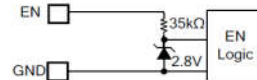
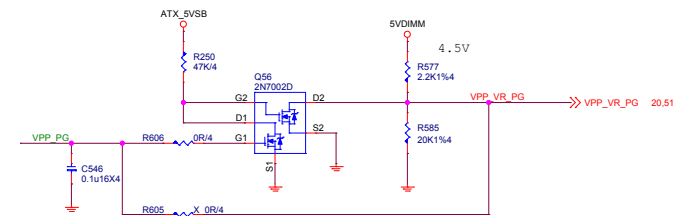
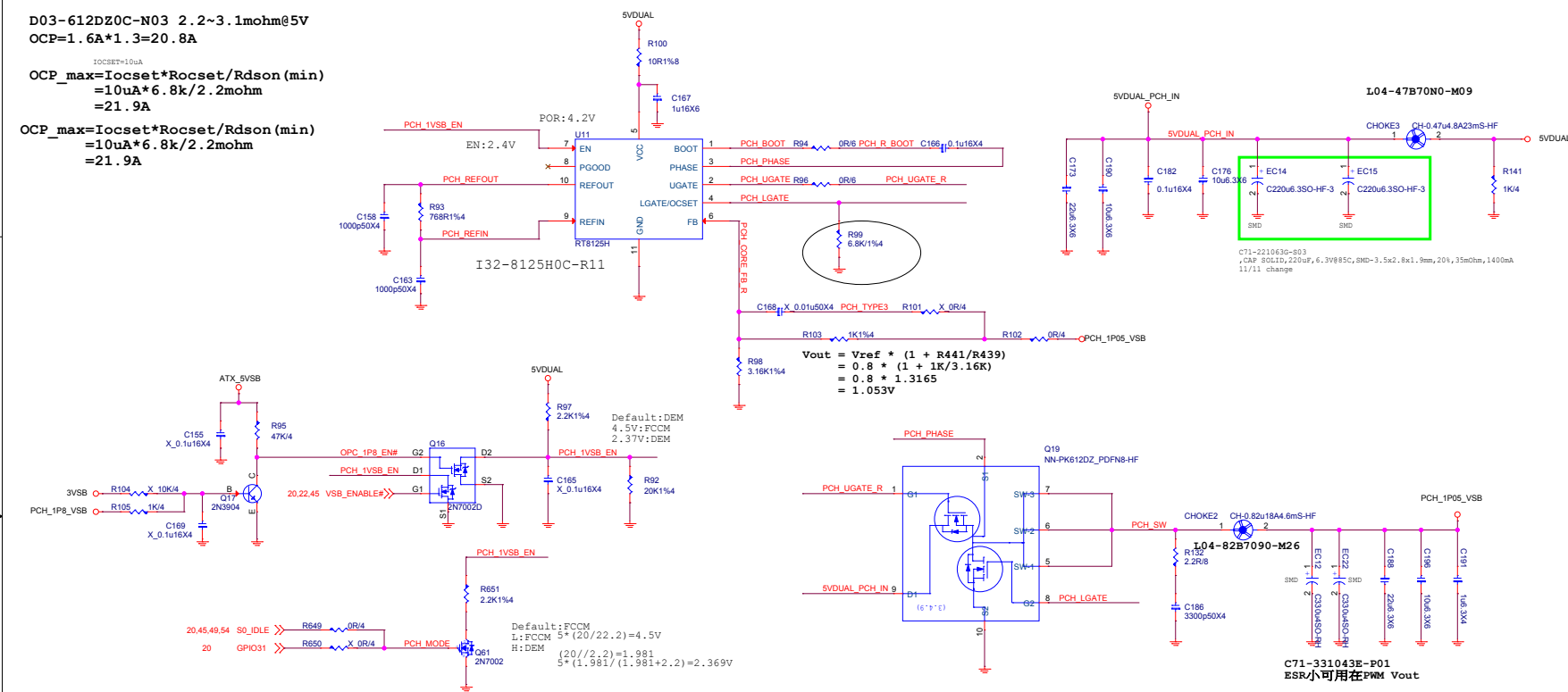


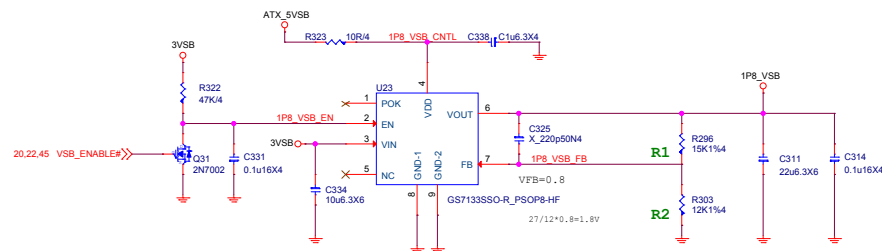
Figure 2: Zener Diode between EN and GND



D03-612DZ0C-N03 2.2~3.1mohm@5V  
OCP=1.6A\*1.3=20.8A

$$\begin{aligned} \text{IOCSET} &= 10\mu\text{A} \\ \text{OCP\_max} &= \text{Iocset} * \text{Rocset} / \text{Rdson}(\text{min}) \\ &= 10\mu\text{A} * 6.8\text{k} / 2.2\text{mohm} \\ &= 21.9\text{A} \end{aligned}$$
$$\begin{aligned} \text{OCP\_max} &= \text{Iocset} * \text{Rocset} / \text{Rdson (min)} \\ &= 10\mu\text{A} * 6.8\text{k} / 2.2\text{mohm} \\ &= 21.9\text{A} \end{aligned}$$


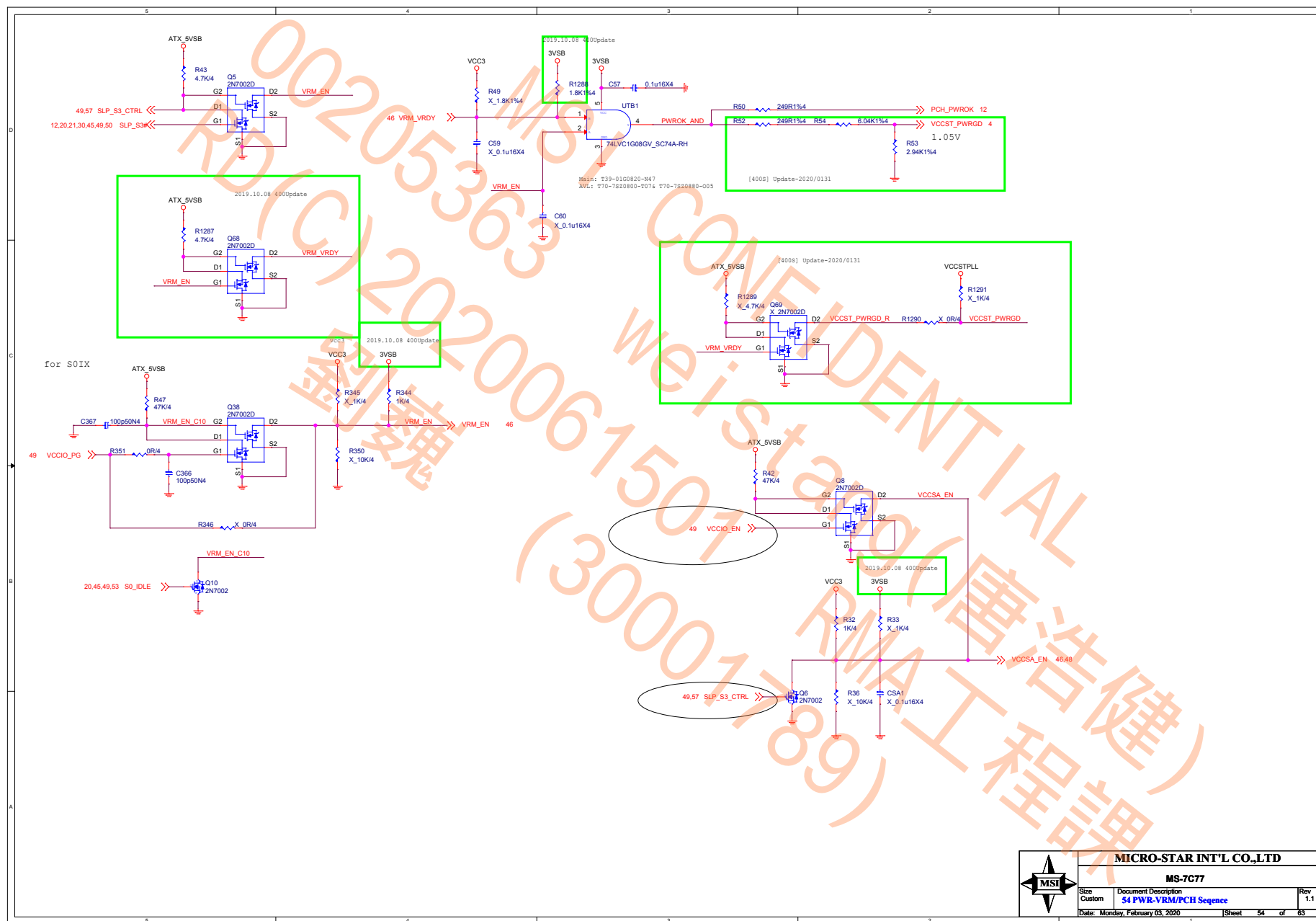
ATX\_5VSB



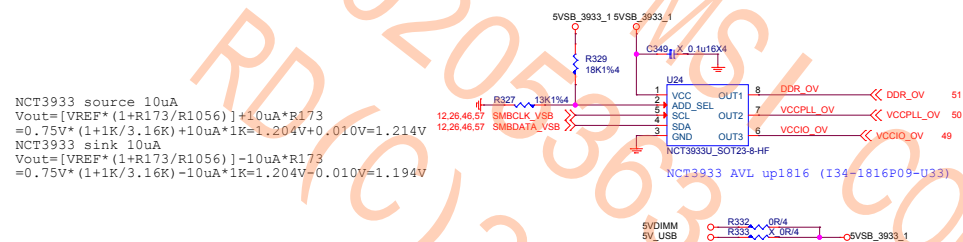
MS-7C77

Size Custom	Document Description <b>53 PCH POWER-RT8125E/IP8_VSB</b>
Date: Friday, January 31, 2020	Sheet 5

Rev	1.1
-----	-----



0x26: RH=18K, RL=13K



```
NCT3933 source 10uA
Vout=[VREF*(1+R173/R1056)]+10uA*R173
=0.75V*(1+1K/3.16K)+10uA*1K=1.204V+0.010V=1.214V
NCT3933 sink 10uA
Vout=[VREF*(1+R173/R1056)]-10uA*R173
=0.75V*(1+1K/3.16K)-10uA*1K=1.204V-0.010V=1.194V
```

NCT3933 AVL up1816 (I34-1816P09-U33)



**MICRO-STAR INT'L CO.,LTD**

MS-7C77

Size	Document Description
Custom	55 OV-NCT3933

Rev	1.1
-----	-----

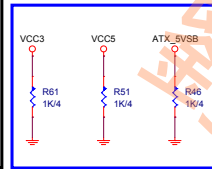
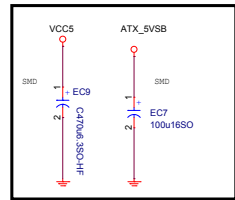
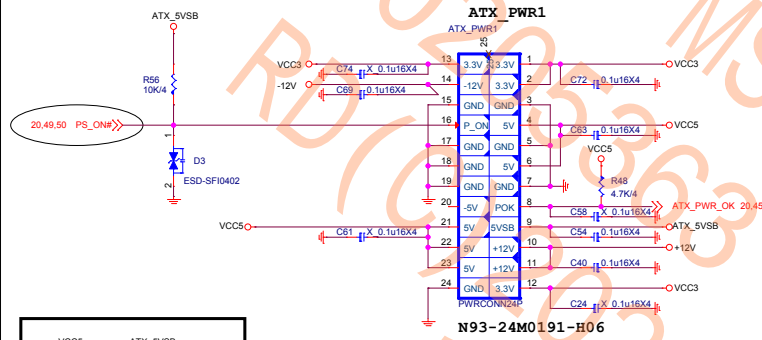
Date: Friday, January 31, 2020

Sheet 55 of 63

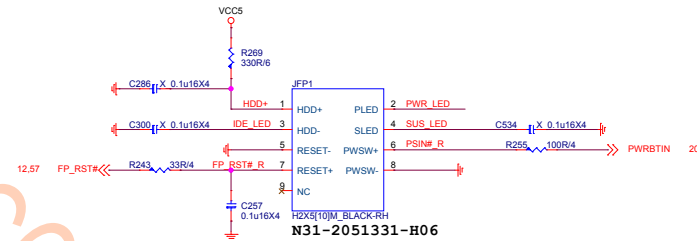


## ATX POWER CONNECTOR

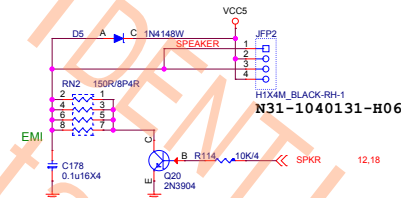
note: If -12V/-5V pin were connected, remember add MLC cap close pin



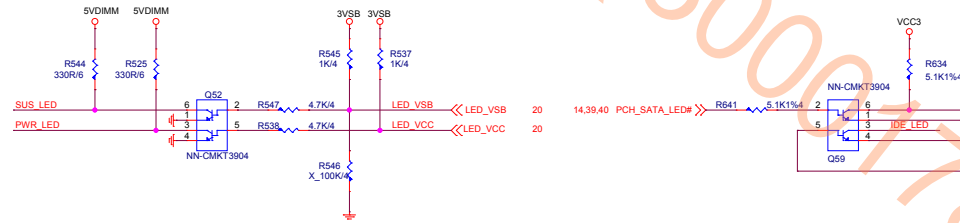
## FRONT PANNEL



## Speaker Pin Header



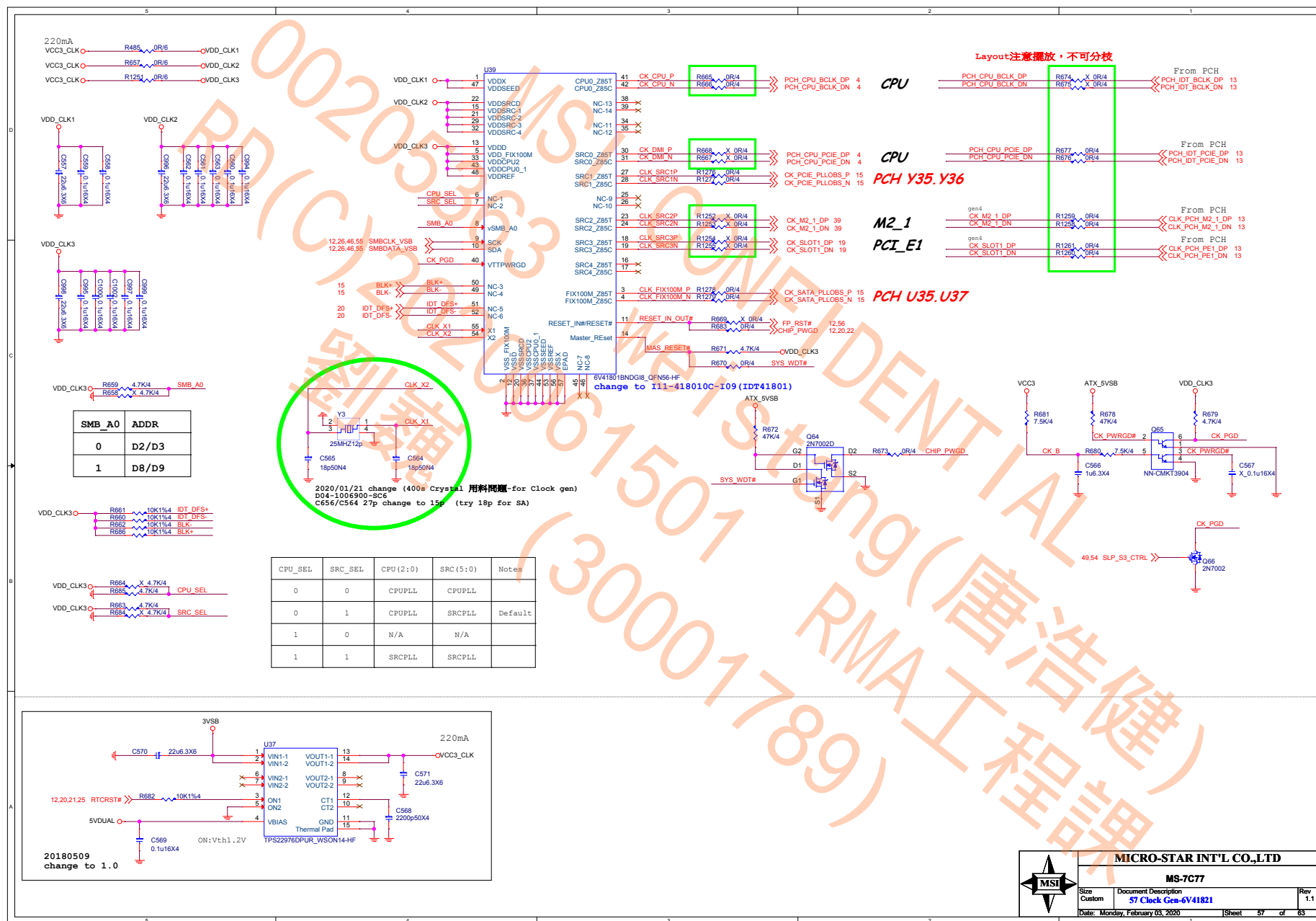
## FRONT PANEL LED

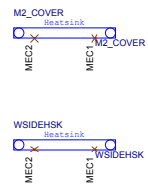


MICRO-STAR INT'L CO.,LTD

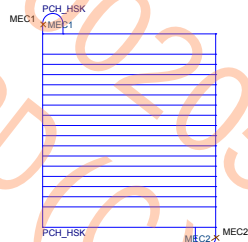
MS-7C77

Size Custom Document Description 56 ATX F\_Panel Rev 1.1  
Date: Friday, January 31, 2020 Sheet 56 of 63

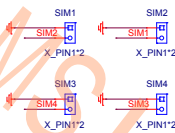




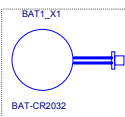
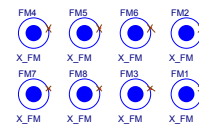
PCH\_SINK E31-0410430-A87  
M2\_COVER E31-0002490-A87  
W MOS SINK E32-0500070-A87



#### Simulation

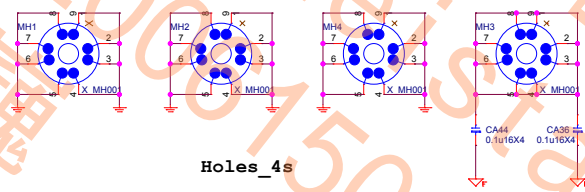


#### Optical Fiducial Marks-120



PD0-07C7711-G37, 精成-深圳, 170, 寶安恩斯邁廠 (MSIS)

#### Mounting Holes



Holes\_4s



MICRO-STAR INT'L CO.,LTD

MS-7C77

Size	Document Description	Rev
Custom	57 Manual Parts	1.1
Date: Friday, January 31, 2020	Sheet 56 of 63	